



Scientist (m/f/d) for Fault-Tolerant Design Methodology

Job-ID: 4096/19 | Dept: System Design | Working Time: 20h/week | Salary: according TV-L | Limitation: initially 1 year with option of extension
Entry Date: 01.11.2019

IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultra high-frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 330 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with 0.13/0.25 μm BiCMOS technologies, located in a 1000 m^2 class 1 cleanroom.

The Research:

- Analysis of resilient architectures addressing synergistically timing and SEE faults
- Integration and evaluation of resilient architectures on realistic adaptive multi-processor platform
- Exploring of adaptivity in multi-processor platform for performing the trade-off between the reliability/power consumption/performance
- A PhD degree can be pursued and will be encouraged

Your Qualifications:

- Master degree in Electrical or Computer Engineering
- Strong background in digital circuit architecture and design
- Experience in VHDL and front-end ASIC design flow and tools (such as Cadence and Synopsys)
- Very good oral and written skills in English

Our Offer:

Do research in a challenging, multinational environment, with excellent career prospects. You will have the opportunity to establish an international reputation at the forefront of high tech. It is important to us to support the individual career developments of our employees (e.g. conferences, advanced trainings). More information about our scientific excellence and the working environment at IHP can be found on our website.

IHP seeks to incorporate more women into the science field. Therefore women are strongly encouraged to apply. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

This is a part-time position (50%) and working time consists of 20 hours per week.

Your application:

Have we sparked your interest? Then we look forward to receiving your application via our [online application form](#).

For further information regarding the position please contact Prof. Dr. Milos Krstic:
krstic@ihp-microelectronics.com

