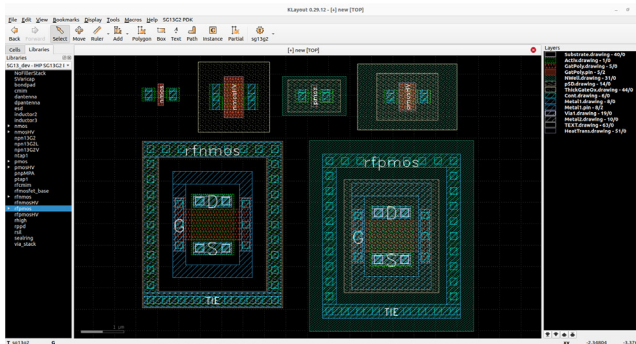
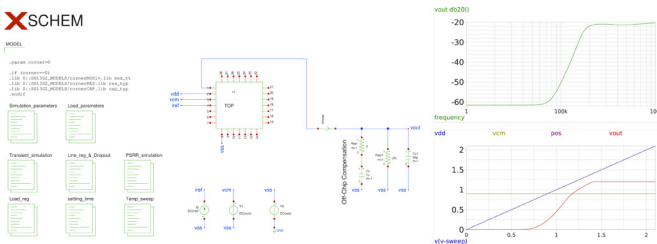


Qucs schematic capture support for RF applications Ngspace and Xyce simulation support



MOSFET parametric layout devices in KLayout



An example of an LDO circuit diagram drawn in the xschem editor including blocks for simulation configuration and graphical visualization of simulated waveforms

**Leveraging community efforts, public funding and corporate contributions**



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[www.ihp-microelectronics.com](http://www.ihp-microelectronics.com),  
[ihp@ihp-microelectronics.com](mailto:ihp@ihp-microelectronics.com)

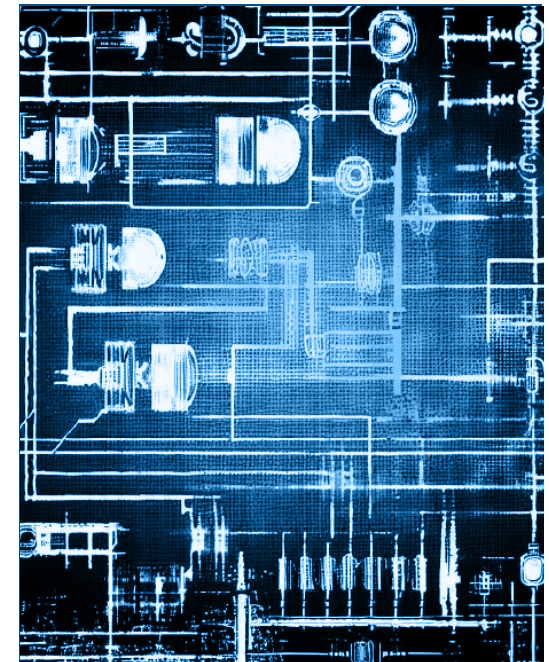
Contact:  
Sergei Andreev  
[andreev@ihp-microelectronics.com](mailto:andreev@ihp-microelectronics.com)



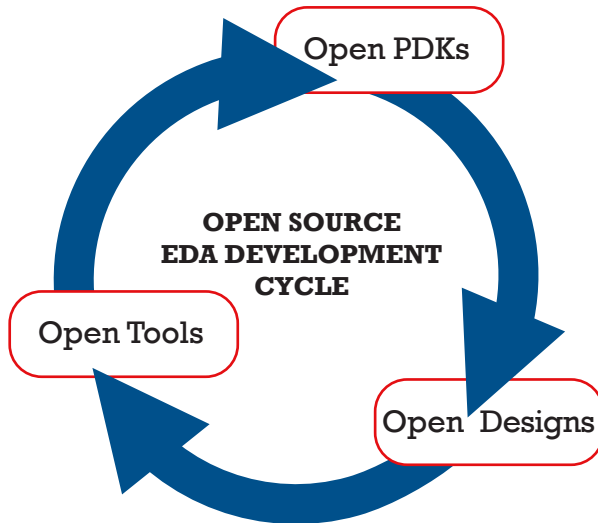
09/2025



**Open PDK and MPW access for IHP BiCMOS Technology**



Enable open and worldwide collaboration on development of the PDK, co-development of tools and PDK features



Provide test cases, regression tests, benchmarks, use cases, user stories, feedback & error reports

Provides platform for open EDA software development and possibility for collaborative design projects

130nm BiCMOS Open Source PDK, dedicated for Analog, Mixed Signal and RF Design.

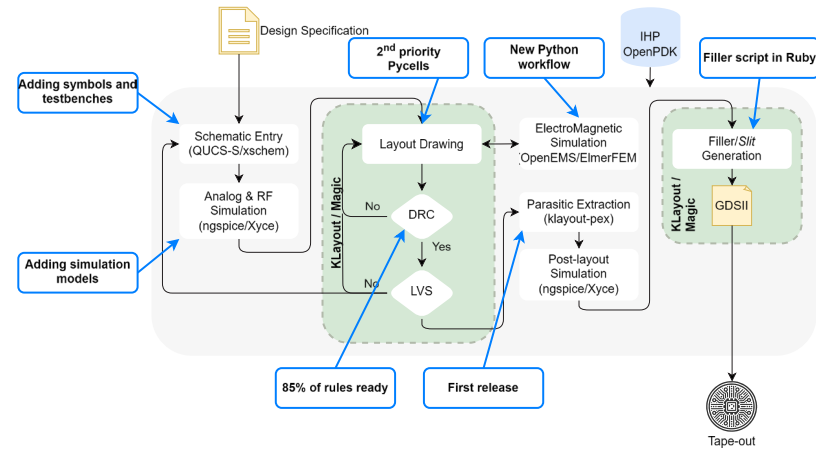
IHP Open Source PDK project goal is to provide a fully open source Process Design Kit and related data, which can be used to create manufacturable designs at IHP's facility.

This Github repository is targeting the SG13G2 process node.

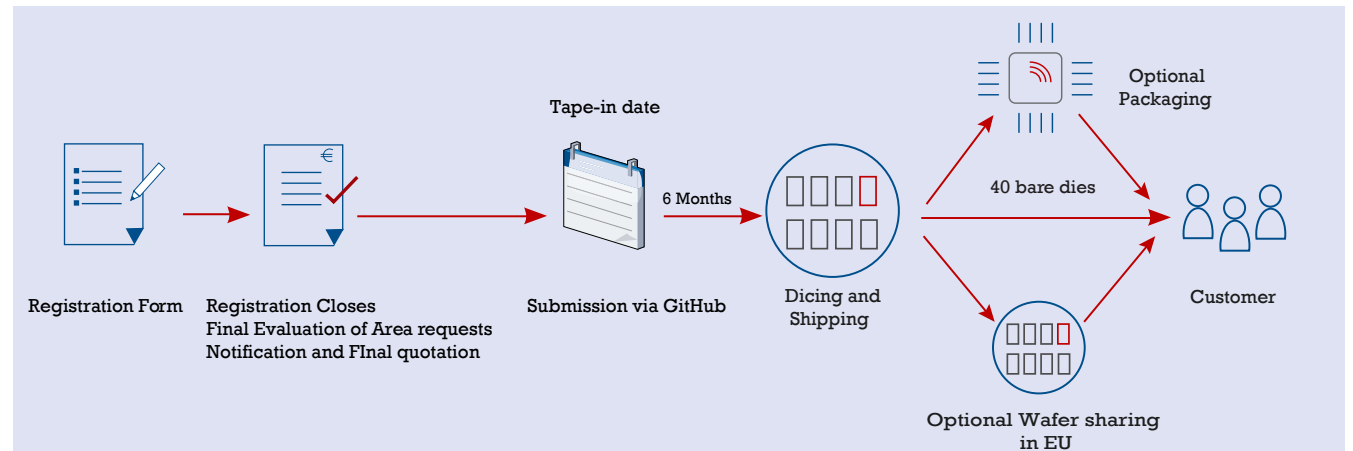
Our PDK has an integrated Chat GPT support.



## Analog/RF Open PDK Design Flow



## IHP-Low Cost Open MPW Concept in a Nutshell



## Schedule of Low Cost MPW Runs

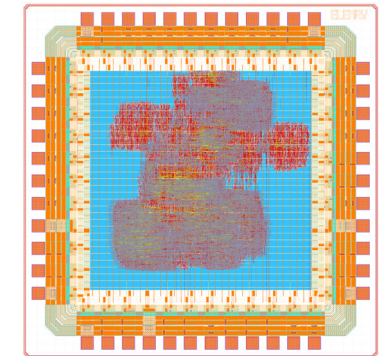
| Tape-in Date | 24.Nov.2025 | 30.Mar.2026 | 05.Oct.2026 | Nov.2026 |
|--------------|-------------|-------------|-------------|----------|
| SG13CM0S     | May,2026    | Sept,2026   | -           | May,2027 |
| SG13G2       | -           | -           | March,2027  | -        |

For terms and conditions check out the QR code!



| Total Area Requested | Pricing                      |
|----------------------|------------------------------|
| <100 sq mm           | Minimum Demand Not Fulfilled |
| 100-150 sq mm        | Area Dependent Price         |
| > 150 sq mm          | Fixed Price                  |

Expand your design opportunities



Layout of RISC-V processor made with elements available within IHP-Open-PDK