



Engineer (m/f/d) for packaging and interconnection technology

Job-ID: 3079/22 | Department: System Architectures | Salary: as per tariff, up to EG 10 (according to degree); recognition of professional experience within the framework of TV-L possible | Working time: 40h/week (part-time work option) | Fixed term: initially 2 years, deferral is intended | Start: as soon as possible

IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultra high-frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 350 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with 0.13/0.25 μm -SiGe-BiCMOS technologies, located in a 1500 m² cleanroom that meets the highest industrial nanotechnology requirements.

Your Position:

As a member of the research group Integration Lab at IHP within the department System Architectures, you will be responsible, together with an experienced colleague, for all questions and work related to packaging and interconnect technologies with a focus on chip and wire bonding. The lab is part of the System Architectures department and provides its services to the entire IHP. Therefore, your tasks include not only managing the equipment, but also advising colleagues from engineering and research on the technical capabilities of the integration lab.

You manufacture and integrate electronic components and contribute your own ideas for the design of processes at board, module and package level. With your work, you also ensure conscientious and accurate documentation, qualification and quality assurance of individual processes. You will support the test lab to about 20% and operate VLSI testers for standard test tasks. You will be trained and accompanied closely by a very experienced colleague during a structured familiarization phase of six months.

You will be assigned appropriate tasks depending on your qualifications and experience, so that you can gradually acquire the specialized expertise you need to work independently in the integration lab. The position is therefore suitable for applicants with a degree as a skilled worker with work experience, as well as for graduates of technical courses of study.

An international team of researchers including very experienced senior and junior scientists as well as PhD students is looking forward to welcoming you in their team. Flat hierarchies and mutual support are



important to us. We see diversity of perspectives as a great advantage for our team and strive for a balanced gender mix.

Your profile:

You hold successfully completed technical studies or completed technical vocational training with work experience. You are preferably experienced in semiconductor technologies and you have creativity and experimental skills for finding technical solutions and optimizing processes. Finally, outgoing and communicative personality with the ability to work with a wide variety of colleagues from all over the world is welcome.

You are also a strong team player. We are looking for a team member, who is able to structure his or her own work and to bring a well-organized and systematic way of working into the cooperation with creative minds. You are an ideal match for this position, when you have experimental, analytical and problem-solving skills, very strong communicative skills and the ability to quickly learn how to operate the latest technical equipment including various software. It is necessary that you confidently handle the English language. Knowledge of the German language is welcome. The consolidating of German language skills is expected and highly encouraged, for example in in-house language courses and intensive courses.

Our offer:

Conducting research in a challenging, multinational environment giving you excellent career opportunities. You will have the chance to establish international reputation at the edge of top-notch technologies. An orientation guide will help you to quickly integrate into the institute and to familiarize yourself with the field.

It is important to us to support the individual career developments (e.g. conferences, advanced trainings) as well as the personal needs of our employees by offering flexible working hours and the possibility to work off-site. The compatibility of work and family is highly valued. More information about our scientific excellence and the working environment at IHP can be found on our website.

IHP is TOTAL E-QUALITY-certified for equal opportunities for women and men at work and actively pursues the equality of all gender and all groups of people. We promote the professional development of women and strongly encourage them to apply. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

Your application:

Have we sparked your interest? Then we look forward to receiving your application via our [online application form](#).

For further information about the position, please contact Mr. Jan Schäffner: career@ihp-microelectronics.com

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