



## Intern or student assistant position (m/f/d) for “Implementing and evaluating a development flow for FPGA ICs with IHP technology”

Job-ID: 30811/22 | Department: System Architectures | Limitation: initially 6 months with the option of extension or culminating into a masters thesis | Entry Date: November 1, 2022 | We offer the possibility to work parallel as a student or research assistant, remuneration according to the guidelines of the state Brandenburg on the working conditions of research and student assistant.

IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultra high-frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 350 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with 0.13/0.25  $\mu\text{m}$ -SiGe-BiCMOS technologies, located in a 1500 m<sup>2</sup> cleanroom that meets the highest industrial nanotechnology requirements.

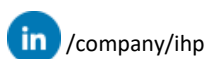
### The position:

As a member of the Fault Tolerant Computing research group within the department System Architectures you will contribute to research into designing and implementing FPGA ICs in IHP technology with the help of open source FPGA development tools. Your tasks will include the setup and testing of the OpenFPGA framework for rapid prototyping of customizable FPGA architectures. An international team of 12 scientists including very experienced senior researchers as well as several PhD students is looking forward to you. Flat hierarchies and mutual support are important to us. We see diversity of perspectives as a great advantage for our team. We strive for a balanced gender mix in our team.

### Your qualifications:

You hold a Bachelor's degree in Computer Sciences or a comparable study area. You are already experienced with HDL design and FPGA programming and have a good understanding of the FPGA architecture. Ideally, but not mandatory you have experience in working with Linux and are familiar with using Makefiles and the general concept of build flows.

You are also a strong team player. We are looking for a team member, who is able to structure his or her own work and to bring a well-organized and systematic way of working into the cooperation with creative minds. You are an ideal match for this position, when you have experimental, analytical and problem-solving skills, very strong communicative skills and the ability to quickly learn how to operate the latest technical equipment including various software. It is necessary that you confidently handle the English language. Knowledge of the German language is





welcome. The consolidating of German language skills is expected and highly encouraged, for example in in-house language courses and intensive courses.

**Our Offer:**

You have the possibility to work in a dynamic and multinational research institute for microelectronics with flexible working hours. Combine your theoretical knowledge from university with practical working experience.

IHP aims to achieve a balanced gender composition in the workforce. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

**Your application:**

Have we sparked your interest? Then we look forward to receiving your application via our [online application form](#).

For further information about the position, please contact Dr.-Ing. Markus Ulbricht: [career@ihp-microelectronics.com](mailto:career@ihp-microelectronics.com).