



Intern or student assistant position (m/f/d) for “Implementing and evaluating a development flow for FPGA ICs with IHP technology”

Job-ID: 3026/24 | Department: System Architecture | Working Time: 19h/week | Limitation: 3-6 months | Entry Date: as soon as possible | Salary (only if student job): as per Guideline of the State of Brandenburg on the Working Conditions of Scientific and Student Assistants

IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultra high-frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 380 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with 0.13/0.25 μm -SiGe-BiCMOS technologies, located in a 1500 m² cleanroom that meets the highest industrial nanotechnology requirements.

The position:

As a member of the Fault Tolerant Computing group within the System Architectures department you will contribute to research into designing and implementing FPGA ICs in IHP technology with the help of open source FPGA development tools. Your detailed tasks will include:

- The setup and testing of the OpenFPGA or FABulous framework for rapid prototyping of customizable FPGA architectures
- The development of test circuits and smaller IPs as prototypes
- Tape-out and testing of a sample circuit in IHP 130nm technology

An international team of 11 researchers including very experienced senior and junior scientists as well as PhD students is looking forward to welcoming you in their team. Flat hierarchies and mutual support are important to us. We see diversity of perspectives as a great advantage for our team and strive for a balanced gender mix.

Your qualifications:

You hold a Bachelor's degree in Computer Sciences or a comparable study area. You are already experienced with HDL design and FPGA programming and have a good understanding of the FPGA architecture. Ideally, but not mandatory you have experience in working with Linux and are familiar with using Makefiles and the general concept of build flows. Finally, you are also a strong team player and confidently handle the English language.



Our Offer:

Conducting research in a challenging, multinational environment giving you excellent career opportunities. You will have the chance to establish international reputation at the edge of top-notch technologies.

It is important to us to support the individual career developments (e.g. conferences, advanced trainings) as well as the personal needs of our employees by offering flexible working hours and the possibility to work off-site. The compatibility of work and family is highly valued. More information about our scientific excellence and the working environment at IHP can be found on our website.

IHP is TOTAL E-QUALITY-certified for equal opportunities for women and men at work and actively pursues the equality of all gender and all groups of people. We promote the professional development of women and strongly encourage them to apply. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

Your application:

Have we sparked your interest? Then we look forward to receiving your application via our [online application form](#).

For further information regarding the position please contact Dr.-Ing. Markus Ulbricht: career@ihp-microelectronics.com.



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