

IHP GmbH Innovations for High Performance Microelectronics/
Leibniz-Institut für innovative Mikroelektronik



Engineer position (m/f/d) for "Open-source PDK & design course development"

Job-ID: 7024/24 | Department: Technology | Salary: as per tariff (TV-L) | Working Time: 40h/week ((part-time work option) | Limitation: initially till end of 2025 with option of extension | Starting Date: as soon as possible

IHP is an institute of the Leibniz Association and conducts research and development of silicon-based systems and ultra high-frequency circuits and technologies including new materials. It develops innovative solutions for application areas such as wireless and broadband communication, security, medical technology, industry 4.0, automotive industry, and aerospace. IHP employs approximately 380 people. It operates a pilot line for technological developments and the preparation of high-speed circuits with $0.13/0.25 \, \mu m$ -SiGe-BiCMOS technologies, located in a 1500 m² cleanroom that meets the highest industrial nanotechnology requirements.

The position:

As a member of the Research and Prototyping Service group within the Technology department you will contribute to development of IHP's open source Process Design Kit (PDK) and development of design examples for training purpose. Your detailed tasks will include:

- Development of test circuits in open EDA tools
- Verification of existing open source PDK with test circuits and comparison with commercial PDK flow
- Documentation of results to support Design courses

An international team of 11 developers is looking forward to welcoming you in their team. Flat hierarchies and mutual support are important to us. We see diversity of perspectives as a great advantage for our team and strive for a balanced gender mix.

Your qualifications:

You hold a Bachelor's degree in Computer Sciences or a comparable study area with strong background in semiconductors, physics or electrical engineering. Knowledge in semiconductor devices and programming are of advantage. Your specialized knowledge preferably covers ASIC design environments using open source EDA tools. Furthermore, you have skills for programming in scripting languages (e.g. Python, Perl or TCL).

You are also a strong team player. We are looking for a team member, who is able to structure his or her own work and to bring a well-organized and systematic way of working into the cooperation with creative minds. You are an ideal match for this position, when you have experimental, analytical and problem-solving skills, very strong



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PD M03 A13 en Rev. Kc 01/2022



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communicative skills and the ability to quickly learn how to operate the latest technical equipment including various software. It is necessary that you confidently handle the English language.

Our Offer:

Do research in a challenging, multinational environment giving you excellent career opportunities. You will have the chance to establish international reputation at the edge of top-notch technologies. An orientation guide will help you to quickly integrate into the institute and to familiarize yourself with the field.

It is important to us to support the individual career developments (e.g. conferences, advanced trainings) as well as the personal needs of our employees by offering flexible working hours and the possibility to work off-site. The compatibility of work and family is highly valued. More information about our scientific excellence and the working environment at IHP can be found on our website.

IHP is TOTAL E-QUALITY-certified for equal opportunities for women and men at work and actively pursues the equality of all gender and all groups of people. We promote the professional development of women and strongly encourage them to apply. Disabled applicants, qualified according to the above criteria, will be given preference over other candidates with equivalent relevant qualifications.

If you are looking for accommodation in Frankfurt (Oder), our Relocation Service will be happy to assist you.

Further advantages:

30 days holiday | special annual payment | Company pension scheme (VBL) | Flexible working hours, also part-time (no core working hours) | Possibility to work up to 40 % independent of location according to company agreement | Parent-child room as a possibility to work with a child in case of childcare bottlenecks | A wide range of further training opportunities in-house or within the framework of business trips | Discounted company ticket with monthly allowance of € 15 for various fare zones | Good transport connections, free parking at the institute | Canteen with breakfast and lunch | On-site health services | Company family and care guides | Free, confidential counselling by an external service provider in a wide variety of challenging private or professional situations, for example on how to reconcile work and family life or in psychosocial emergencies | Structured induction and actively supported integration into the institute (welcome workshop, intercultural workshop, joint leisure activities)

Your application:

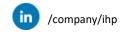
Have we sparked your interest? Then we look forward to receiving your application via our online application form.

For further information regarding the position please contact Dr. Frank Vater: career@ihp-microelectronics.com.









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PD_M03_A13_en Rev. Kc
01/2022