

OPEN ACCESS

Test beam measurement of the first prototype of the fast silicon pixel monolithic detector for the TT-PET project

To cite this article: L. Paolozzi *et al* 2018 *JINST* **13** P04015

View the [article online](#) for updates and enhancements.

Related content

- [4D tracking with ultra-fast silicon detectors](#)
Hartmut F-W Sadrozinski, Abraham Seiden and Nicolò Cartiglia
- [Prototype ATLAS IBL modules using the FE-I4A front-end readout chip](#)
The ATLAS IBL collaboration
- [Status of HVCMOS developments for ATLAS](#)
I. Peri, R. Blanco, R. Casanova Mohr et al.

RECEIVED: February 2, 2018

REVISED: March 16, 2018

ACCEPTED: March 22, 2018

PUBLISHED: April 12, 2018

Test beam measurement of the first prototype of the fast silicon pixel monolithic detector for the TT-PET project

L. Paolozzi,^{a,1} Y. Bandi,^b M. Benoit,^a R. Cardarelli,^c S. Débieux,^a D. Forshaw,^b
D. Hayakawa,^{a,d} G. Iacobucci,^a M. Kaynak,^e A. Miucci,^b M. Nessi,^{a,f} O. Ratib,^d
E. Ripiccini,^{a,d} H. Rücker,^e P. Valerio,^a and M. Weber^b

^aUniversity of Geneva,
Rue du Général-Dufour 24, Geneva, Switzerland

^bUniversity of Bern,
Sidlerstrasse 5, Bern, Switzerland

^cINFN Section of Roma Tor Vergata,
Via Della Ricerca Scientifica 1, Roma, Italy

^dInstitute of Translational Molecular Imaging (ITMI), University of Geneva,
Geneva, Switzerland

^eIHP, Im Technologiepark 25, Frankfurt (Oder), Germany

^fCERN, Geneva, Switzerland

E-mail: lorenzo.paolozzi@unige.ch

ABSTRACT: The TT-PET collaboration is developing a PET scanner for small animals with 30 ps time-of-flight resolution and sub-millimetre 3D detection granularity. The sensitive element of the scanner is a monolithic silicon pixel detector based on state-of-the-art SiGe BiCMOS technology. The first ASIC prototype for the TT-PET was produced and tested in the laboratory and with minimum ionizing particles. The electronics exhibit an equivalent noise charge below 600 e⁻ RMS and a pulse rise time of less than 2 ns, in accordance with the simulations. The pixels with a capacitance of 0.8 pF were measured to have a detection efficiency greater than 99% and, although in the absence of the post-processing, a time resolution of approximately 200 ps.

KEYWORDS: Si microstrip and pad detectors; Timing detectors; Front-end electronics for detector readout; Gamma camera, SPECT, PET PET/CT, coronary CT angiography (CTA)

¹Corresponding author.

Contents

1	Introduction	1
2	Design, simulation and characterization of the first ASIC prototype	3
3	Experimental setup at the CERN SPS test beam	5
4	Test beam results	5
4.1	Efficiency and uniformity of response	5
4.2	Time resolution	7
5	Conclusions	9

1 Introduction

The feasibility of a silicon pin detector with 100 ps time resolution for Minimum Ionizing Particles (MIPs) with 1 pF pixel capacitance was demonstrated in a previous work exploiting a parallel plate readout and a fast low-noise amplifier based on SiGe HBT technology [1]. That result, obtained for a 100 μm thick sensor, ensured that, with the proper sensor geometry and a fast charge-integrating amplifier, the detector time resolution can be expressed as [1–3]:

$$\sigma_t = \frac{\text{Rise time}}{\frac{\text{Signal}}{\text{Noise}}} = \frac{ENC}{\frac{dQ}{dx} v_d}, \quad (1.1)$$

where $\frac{dQ}{dx}$ is the primary charge produced per unit length in the sensor, v_d is the average drift velocity of the charge carriers and ENC is the equivalent noise charge of the preamplifier. The formula does not take into account the irreducible sensor contribution that comes from the charge collection noise (or Landau noise) [4], which contributes approximately 30 ps RMS for a 100 μm thick pin diode [4, 5]. That work was the starting point for the development of the monolithic pixel detector for the Thin-ToF PET (TT-PET) project,¹ which aims to develop a time-of-flight positron emission tomography scanner for small animals based on silicon pixel sensors with 30 ps RMS time resolution for 511 keV photons. Equation (1.1) shows how a larger charge deposition in the sensor is associated to a better time resolution. Starting from that expression, a Geant4 simulation was used to predict that, in the same experimental condition of [1], the conversion of 511 keV photons would produce a time resolution of 30 ps RMS.

The basic TT-PET photon detection element is made by a 50 μm thick lead converter, a 100 μm thick monolithic silicon pixel sensor and a 55 μm thick polyimide flex circuit. The scanner is segmented into 16 equally sized wedge-like structures which are formed by stacking the basic

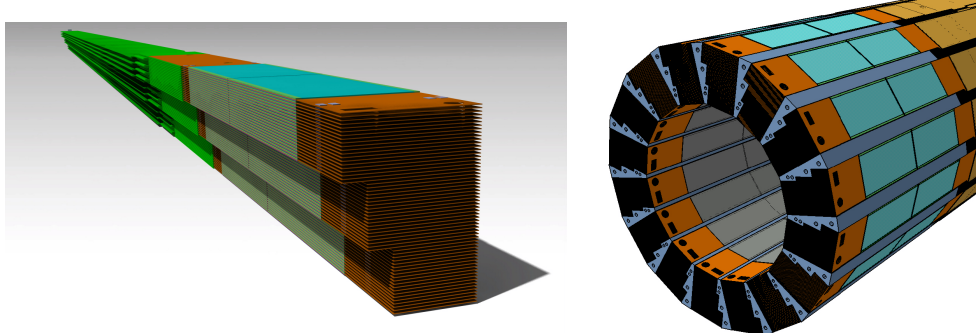


Figure 1. CAD model of a single wedge (left) and the entire TT-PET scanner (right). The light blue elements are the detector ASICs; in orange is represented the polyimide flex circuit connected to the chip and in green the data flex. The grey structures between two the scanner cells are the active cooling elements.

detection element vertically on top of each other 60 times (figure 1). The system will have approximately 1.5 million pixels, with a readout granularity of $500 \times 500 \times 255 \mu\text{m}^3$.

The specifications of the TT-PET chip in terms of power budget, time resolution and read-out channel density required the integration of both the sensor matrix and the front-end electronics in a monolithic structure, using state-of-the-art SiGe BiCMOS technology. A detector with a very high 3D detection granularity necessitates low power consumption and a minimization of the sensor interconnections. The specifications of the analogue components of the final ASIC are shown in table 1.

Table 1. Specifications of the monolithic pixel detector chip for the TT-PET scanner.

ASIC length	24 mm
ASIC width	7, 9, 11 mm
Pixel Size	$500 \times 500 \mu\text{m}^2$
Pixel Capacitance (comprised routing)	$< 750 \text{ fF}$
Power Density Budget	$40 \text{ mW}/\text{cm}^2$
Preamplifier <i>ENC</i> RMS (750 fF input capacitance)	$600 e^-$
Preamplifier Rise Time (10% – 90%)	800 ps
Time Resolution RMS for MIPs	100 ps

The chip will be produced on a $1 \text{ k}\Omega\text{cm}$ resistivity substrate to achieve full bulk depletion and to avoid the breakdown in the inter-pixel region at high voltages. In order to guarantee the uniformity of both the electric field and the weighting field, it will be necessary to thin down to $100 \mu\text{m}$ and back-side metallize the chip. The electric field in the bulk will be of 2 to $3 \text{ V}/\mu\text{m}$, in order to saturate the drift velocity of the charge carriers in silicon.

¹SNSF grant CRSII2-160808.

2 Design, simulation and characterization of the first ASIC prototype

The first monolithic test chip for the TT-PET project was specifically designed to prove the functionality and performance of the front-end electronics on a very high resistivity substrate and verify the high voltage tolerance of the full custom guard ring and inter pixel structures implemented for the first time in a SiGe BiCMOS process. The design was done using the SG13S technology [7] from IHP microelectronics, which provides a fast, low-noise, low-current SiGe HBT for the pre-amplification stage. The production was done using a multi-project wafer via Europractice-IC services.² The chip layout is shown in figure 2.

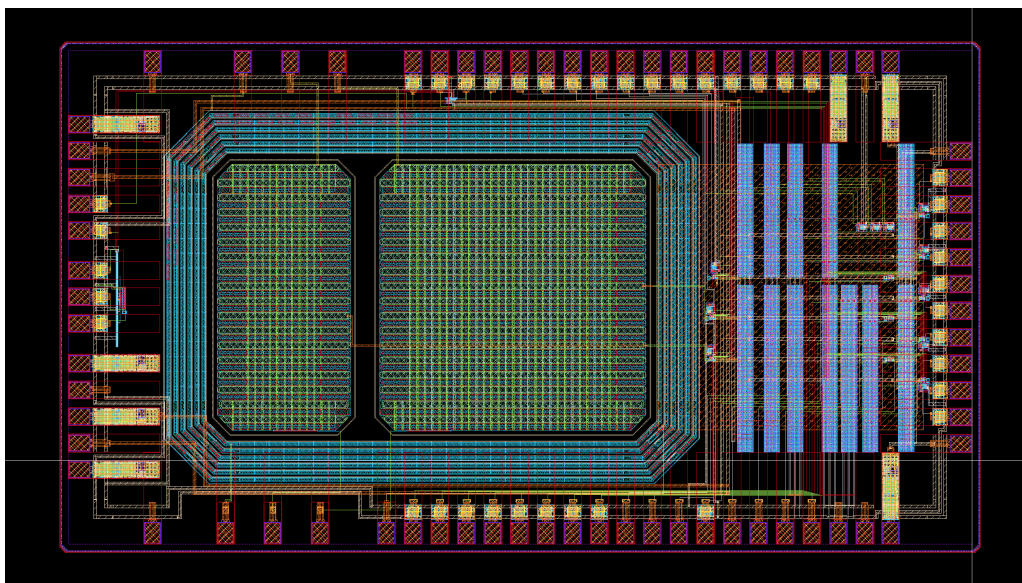


Figure 2. Layout of the first monolithic test chip of the TT-PET project. The guard ring separates the sensitive volume from the front-end electronics.

The chip contains a small ($900 \times 450 \mu\text{m}^2$) and a large ($900 \times 900 \mu\text{m}^2$) n-in-p pixel inside a common guard ring, where the pixels are spaced by $100 \mu\text{m}$. The electronics, placed outside the guard ring, consist of a SiGe-HBT preamplifier and a discriminator with time-over-threshold measurement capability. The chip was produced using a wafer of $1 \text{ k}\Omega\text{cm}$ resistivity. It was not thinned, nor had backside metallization when being characterised at test beam. This prevented the saturation of the drift velocity of the charge carriers and worsened the uniformity of the weighting field for the signal induction. A positive bias voltage was applied directly to each pixel using poly-silicon biasing resistors.

Figure 3 shows the TCAD³ simulation of the sensor depletion profile in the direction crossing both pixels for a bias voltage of 160 V . The simulation shows that the depletion depth is approximately $130 \mu\text{m}$ and is not uniform underneath the pixel area. The electric field, well below the desired 2 to $3 \text{ V}/\mu\text{m}$, is not uniform in depth and is too low to saturate the charge-carriers drift velocities. Both effects are expected to degrade the performance of the un-thinned sensor that we operated in the test beam.

²<http://www.europractice-ic.com/>.

³<http://www.synopsys.com> — Software: Synopsys Sentaurus.

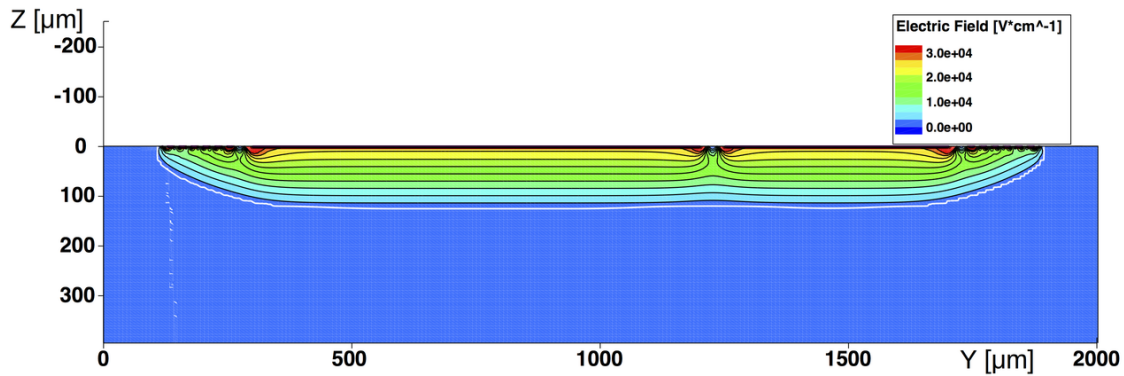


Figure 3. Results of the TCAD simulation: cross section of the sensor showing the depletion region and the electric field intensity underneath the small pixel, large pixel and guard ring. The white line represents the limit of the depletion region. The fact that the thick substrate was not thinned to $100\ \mu\text{m}$ and the absence of the backplane metallization cause an electric field gradient as a function of the sensor depth and prevent the saturation of the drift velocity of the charge carriers.

The capacitance of the small and large pixels have been measured at the same operating point of $160\ \text{V}$ to be $0.8\ \text{pF}$ and $1.2\ \text{pF}$, respectively. For these capacitance values, the expected preamplifier noise (ENC) from Cadence⁴ simulation is less than $600\ \text{e}^-$ RMS for the small pixel and $750\ \text{e}^-$ RMS for the large one. The pulse rise time at the output of the preamplifier is equivalent to the charge collection time. The working voltage is adjustable and has been optimized using Cadence in terms of the contribution to the time jitter and the power consumption (figure 4). The breakdown voltage was measured to be approximately $165\ \text{V}$.

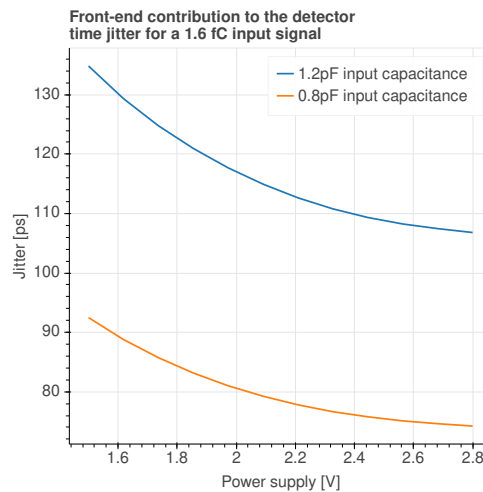


Figure 4. Results of the Cadence simulation: the front-end electronics contribution to the time jitter for a $1.6\ \text{fC}$ input signal as a function of the preamplifier power supply, for the capacitance values of the two pixels under study. This is expected to be the dominating contribution to the time jitter in the parallel-plate approximation with saturated drift velocity of the charge carriers.

⁴<https://www.cadence.com> — Software: Virtuoso ADL.

3 Experimental setup at the CERN SPS test beam

The performance of the monolithic test chip has been measured at the SPS test beam line at CERN with a beam of 180 GeV/c momentum pions. Two chips were glued on two identical test boards with conductive glue, where the substrate was referenced to ground. The discriminator threshold was set individually for each readout channel. The discriminator outputs were transmitted using coaxial lines with 50 Ω impedance.

A 50 μm thick LGAD sensor [8] with 30 ps time resolution produced by CNM [9], placed on a third board and readout by the same electronics of [1], was used as a time reference for the measurements. The signals from the monolithic sensors and the LGAD were sent to an oscilloscope with 1 GHz analogue bandwidth. The three boards were placed downstream with respect to the beam of the Geneva FE-I4 telescope [10], which provided the trigger to the oscilloscope and the trajectory of the pions. Figure 5 shows a schematic representation of the setup.

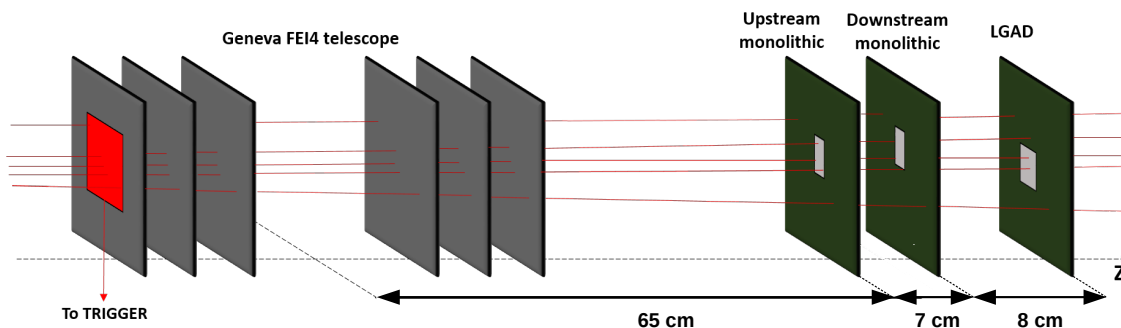


Figure 5. Schematic representation of the experimental setup showing, from left to right, the six planes of the telescope, the upstream and downstream monolithic test chips, and the LGAD. The horizontal red arrows represent the pion beam. The telescope trigger area is larger than the sensitive area of the detectors under test.

All the data presented on this paper were acquired with a bias voltage of 160 V applied to the monolithic pixels and 230 V applied to the LGAD. The amplifier power consumption was 350 $\mu\text{W}/\text{channel}$, corresponding to a simulated noise (ENC) of 750 e^- RMS for the large pixel and 550 e^- RMS for the small pixel. The discriminator signals from the small pixel of the upstream sensor, the large pixel from the upstream sensor, the small pixel of the downstream sensor, and the LGAD were recorded using the four available channels of the oscilloscope.

A total of 1 104 396 events were triggered by the telescope in an area of $3200 \times 2900 \mu\text{m}^2$. A selection of the tracks with a slope within two standard deviations from the mean value of their distribution was used in order to reduce the effect of multiple scattering. After this selection, the dataset consisted of 972 046 events in the triggered region.

4 Test beam results

4.1 Efficiency and uniformity of response

The efficiency map for the upstream sensor and the downstream small pixel are shown in figure 6. The efficiency is calculated by selecting the tracks pointing to a region of the detector under test

and calculating the fraction of events detected by the sensor. The three pixels show a high and uniform efficiency. The areas contoured in black in figure 6 correspond to the region of the pixels selected to study the efficiency. The dashed rectangle is the projection of the LGAD active area on the sensors. The region of interest on which the time resolution has been studied is represented by the red rectangles.

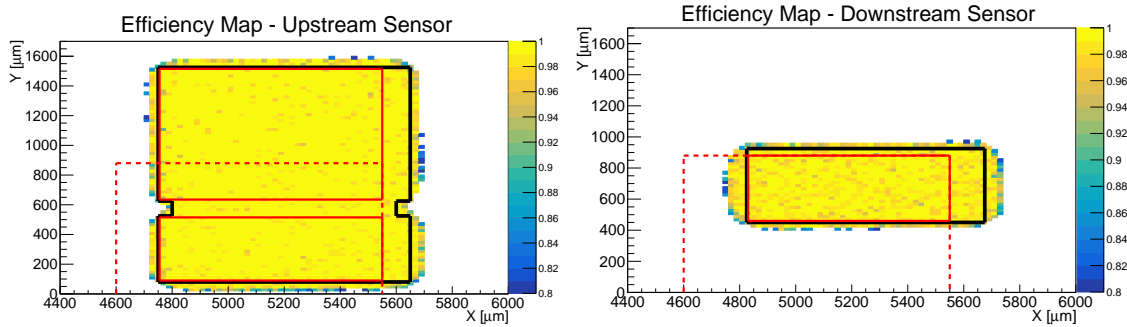


Figure 6. Efficiency map of the upstream chip (left) and the downstream small pixel (right). The areas contoured in black represent the regions of the monolithic pixels selected to measure the efficiency; the dashed rectangles represent the projection of the LGAD active area on the monolithic chips. The red rectangles are the regions of interest used for the time resolution measurement. In the downstream sensor, only the small pixel was read out.

The measured efficiency for MIP detection is $(99.79 \pm 0.01)\%$ for the upstream sensor and $(99.09 \pm 0.04)\%$ for the small pixel of the downstream sensor. The smaller value obtained for the downstream pixel is probably due to the multiple scattering produced by the upstream board, which is reducing the tracking precision of the telescope. In order to reduce this effect, the efficiency of the downstream pixel has been measured in the same region selecting only the events from the telescope that were detected also by the LGAD. The downstream small pixel efficiency with this selection was measured to be $(99.88 \pm 0.04)\%$.

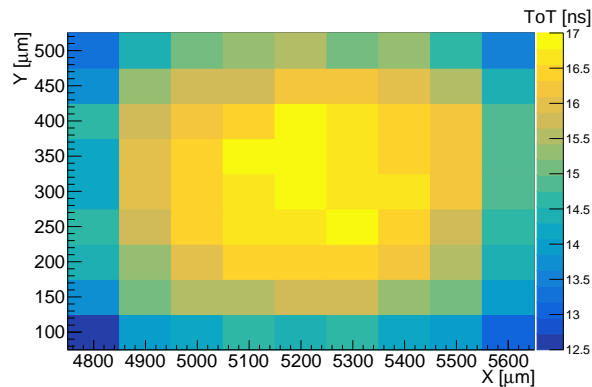


Figure 7. Map of the most probable time-over-threshold values for the upstream small pixel. For small signals, the time-over-threshold at the output of the amplifier is proportional to the charge deposited in the pixel.

The uniformity of response over the pixel area was also studied. The most probable time-over-threshold (ToT) of the signal was used to estimate the charge deposited in the different regions of

the depleted sensor volume. Figure 7 shows the map of the most probable ToT on the small pixel of the upstream chip. The average charge is measured to be smaller in the region close to the pixel edge, with $\sim 15\%$ less charge compared to the center of the pixel. As shown before, the TCAD simulation presented in figure 3 indicates that the depletion depth was expected to be constant in the selected area. We conclude from the ToT measurement that the simulation is not able to reproduce the effect, probably because it does not contain the models for the charge sharing and the weighting field for fast signals when the reference ground is made by high-resistivity silicon.

Device operation at 99% efficiency with lower charge collection in the edges enables the estimation of the upper limit for the amplifier ENC . Laboratory measurement of the error function of the small and large pixels, shown in figure 8, allow for the nominal threshold to be expressed as a function of voltage RMS noise of the amplifier, σ_V . The thresholds for the small and large pixels are $V_{th}^{small} = 9.5 \sigma_V^{small}$, and $V_{th}^{large} = 6.8 \sigma_V^{large}$, respectively. Given a minimum primary charge $Q_{MIN} = 6250 e^-$ from a MIP traversing $130 \mu m$ of depleted silicon simulated by Geant4 [11], which should be reduced by 15% due to the lower charge collection measured in the edges, the upper limit for the ENC that allows operating at the plateau of efficiency is:

$$ENC_{SMALL} \lesssim 0.85 \cdot \frac{Q_{MIN}}{9.5} = 560 e^- \text{ RMS} \quad (4.1a)$$

$$ENC_{LARGE} \lesssim 0.85 \cdot \frac{Q_{MIN}}{6.8} = 780 e^- \text{ RMS} \quad (4.1b)$$

This result is compatible with expectations from the Cadence Spectre simulations.

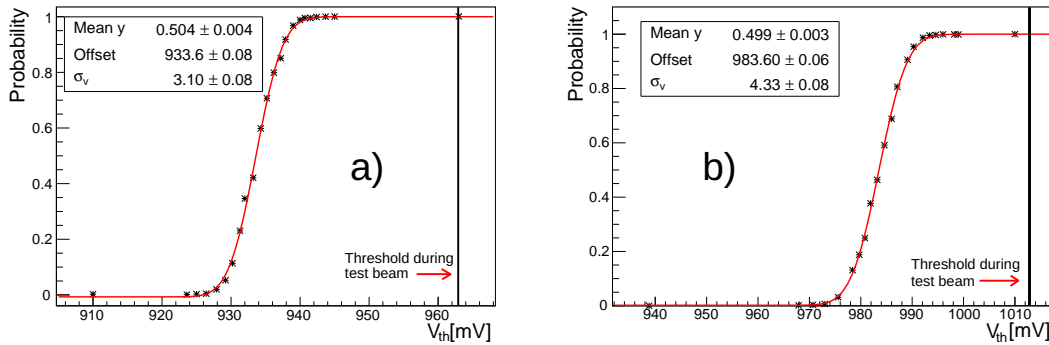


Figure 8. Average output state of the discriminator as a function of the threshold value for the upstream small pixel (a) and upstream large pixel (b). The curves were fitted with an error function; the output parameters of the fits are shown in the panels. The vertical lines show the value of the thresholds used during the test beam measurements.

4.2 Time resolution

The time resolution of the three pixels under study has been measured in the region on interest described in figure 6. Figure 9 shows the ToT distribution and the time walk for the small pixel of the upstream sensor. The time walk correction is performed using the polynomial function resulting from the fit. The spread of the time walk distribution and the signal-to-noise ratio allows for the estimation of the signal peaking time at the output of the amplifier; we find it to be less than 2 ns, consistent with the simulation for the present configuration of the detector.

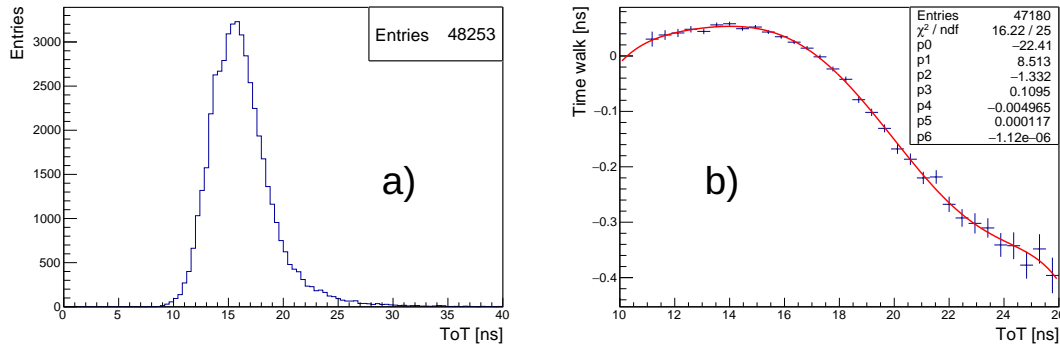


Figure 9. a) Time-over-threshold distribution (a) and average time walk of the signal as a function of the time-over-threshold (b) for the small pixel of the upstream sensor. The vertical offset is arbitrary. The time walk correction is done by fitting the data with a polynomial function.

The time resolution after the time walk correction of the small pixel from the upstream sensor is shown in figure 10 (a). This result is degraded by the non-uniformity of the response of the sensor due to the lack of backplane metallization. This effect can be partly corrected by measuring the variation of the average time-of-flight as a function of the particle hit position on the sensor surface. Figure 10 (b) shows the time resolution obtained for the same pixel after this correction. Following the same analysis procedure, the time resolution measured for the three pixels under study, before and after the correction for the particle hit position, is reported in table 2.

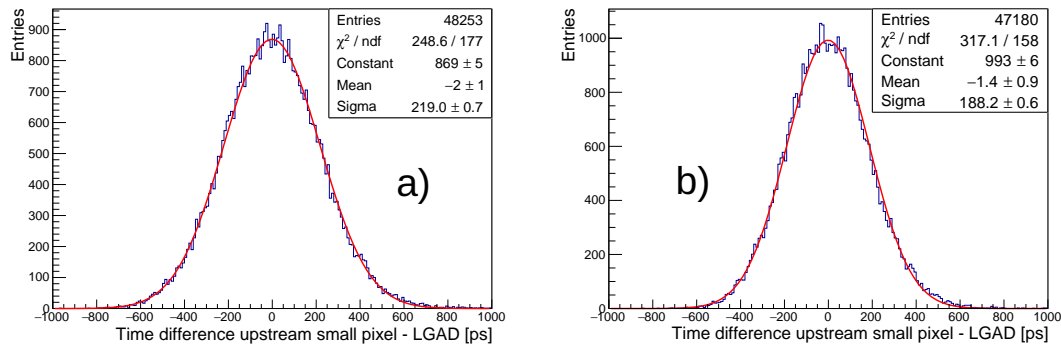


Figure 10. Measurement of the jitter on the time difference between the small pixel of the upstream monolithic sensor and the LGAD without correcting for the particle hit position (a) and correcting for it (b). The mean value of the time difference distributions is set to zero by the time walk correction. The data are fitted with a Gaussian distribution to measure the standard deviation.

The large pixel shows $\sim 20\%$ worse time resolution with respect to the small pixels. Since the ENC is proportional to the pixel capacitance, the difference is the detector capacitance of a factor 1.5 between the large and the small pixels under study, should lead to an increase of the time resolution by the same factor. The smaller difference measured implies that the time resolution is affected, for both pixels, by an additional term, which is not proportional to the amplifier ENC . This term could be attributed to the non-uniformity of the electric field, the thick substrate and the absence of the sensor backplane metallization.

Table 2. Measured time resolution for the monolithic sensor in SiGe BiCMOS technology for MIPs. The contribution from the LGAD sensor is neglected. The capacitance of the small and large pixels is 0.8 and 1.2 pF, respectively.

Pixel	Time resolution [ps]	
	w/o position correction	with position correction
Downstream Small	202.3 ± 0.8	167.7 ± 0.7
Upstream Small	219.0 ± 0.7	188.2 ± 0.6
Upstream Large	265 ± 1	212 ± 1

5 Conclusions

A test beam measurement of the noise, efficiency, and time resolution of the first prototype of the TT-PET monolithic silicon pixel detector in the SiGe BiCMOS technology SG13S from IHP was performed at the CERN SPS with 180 GeV/c pions. The detector, optimized for precise time measurement, has a preamplifier equivalent noise charge of less than $600 e^-$ RMS for a 0.8 pF pixel capacitance, with a total time walk of less than 1 ns for minimum-ionizing particle signals. This performance is obtained with a power consumption of $350 \mu\text{W}/\text{channel}$. The detectors tested show an efficiency larger than 99%. Despite the absence of crucial chip processing steps, such as wafer thinning and backplane metallization, the time resolution is measured to be approximately 200 ps RMS for pixels with 0.8 pF capacitance.

Acknowledgments

The authors wish to thank Florentina Manolescu from the CERN Bondlab for her excellent work, which made the test beam possible, Gabriel Pelleriti from the University of Geneva for the preparation of the detector board and the staff of SPS North Area at CERN for providing excellent quality beam for the whole duration of the test. A particular thank goes to Sebastian Grinstein and his colleagues for providing the LGAD sensor used as a time reference for this test. We are grateful to the colleagues of the University of Geneva who collaborated to the setup and operation of the test beam telescope. Finally, the authors wish to thank the Swiss National Science Foundation, which supported this research.

References

- [1] M. Benoit et al., *100 ps time resolution with thin silicon pixel detectors and a SiGe HBT amplifier*, 2016 *JINST* **11** P03011 [[arXiv:1511.04231](https://arxiv.org/abs/1511.04231)].
- [2] R. Cardarelli, A. Di Ciaccio and L. Paolozzi, *Development of multi-layer crystal detector and related front end electronics*, *Nucl. Instrum. Meth. A* **745** (2014) 82.
- [3] N. Cartiglia et al., *Design optimization of ultra-fast silicon detectors*, *Nucl. Instrum. Meth. A* **796** (2015) 141.
- [4] L. Paolozzi, *Development of particle detectors and related Front End electronics for sub-nanosecond time measurement in high radiation environment*, Ph.D. Thesis, University of Rome Tor Vergata, Rome Italy (2014), http://www.infn.it/thesis/thesis_dettaglio.php?tid=11828.

- [5] W. Riegler and G. Aglieri Rinella, *Time resolution of silicon pixel sensors*, 2017 *JINST* **12** P11017 [[arXiv:1706.04883](#)].
- [6] Y. Bandi et al., *The TT-PET project: a thin TOF-PET scanner based on fast novel silicon pixel detectors*, 2018 *JINST* **13** C01007.
- [7] H. Rucker et al., *A 0.13 μm BiCMOS technology featuring f_T / f_{max} of 240/330 GHz and gate delay below 3 ps*, *IEEE J. Solid-State Circ.* **45** (2010) 1678.
- [8] H.F.W. Sadrozinski et al., *Ultra-fast silicon detectors*, *Nucl. Instrum. Meth. A* **730** (2013) 226.
- [9] J. Lange et al., *Gain and time resolution of 45 μm thin Low Gain Avalanche Detectors before and after irradiation up to a fluence of 10^{15} n_{eq}/cm^2* , 2017 *JINST* **12** P05003 [[arXiv:1703.09004](#)].
- [10] M. Benoit et al., *The FE-I4 Telescope for particle tracking in testbeam experiments*, 2016 *JINST* **11** P07003 [[arXiv:1603.07776](#)].
- [11] J. Allison et al., *Recent developments in GEANT4*, *Nucl. Instrum. Meth. A* **835** (2016) 186.