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Channel characterization of a dual-band dual-polarized SAR with digital beamforming

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Abstract

This paper presents the integration and channel characterization of a highly integrated dualband digital beamforming space-borne synthetic aperture radar (SAR) receiver. The proposed SAR sensor is a low-cost, lightweight, low-power consumption, and dual-band (X/Ka) dualpolarized module ready for the next-generation space-borne SAR missions. In previous works, by the authors, the design and experimental characterization of each sub-system was already presented and discussed. This work expands upon the previous characterization by providing an exhaustive experimental assessment of the fully integrated system. As it will be shown, the proposed tests were used to validate all the instrument channels in a set-up where the SAR sensor was illuminated by an external source minim the ground reflected waves. Test results demonstrate how the system channels are properly operating allowing the reception of the input signals and their processing in the digital domain. The possibility to easily implement a calibration procedure has also been validated to equalize, in the digital domain, the unavoidable amplitude differences between the different channels.

Introduction

Space-borne synthetic aperture radars (SAR) can provide earth observation (EO) under almost any weather conditions during night or daytime. SAR systems can improve the resolution beyond the limitation of physical antenna aperture exploiting the antenna movement and using signal processing. The forward motion of the antenna is indeed used to mime the behavior of a very long array. Thanks to this approach, a high resolution which would require an incredibly large antenna aperture can be obtained with SAR antenna structures of the reasonable size which can be installed onboard satellites or airplanes. Next-generation space-borne SAR missions will employ digital beam-forming (DBF) to provide better resolution, wider coverage, and higher flexibility in general [1, 2]. Compared to phased arrays using analogue beamforming, DBF is much more powerful as it can form multiple steerable beams towards different targets simultaneously, adaptive beam shaping, and have more flexibility [3–5].

The European project DIFFERENT (acronym for "digital beamforming for low-cost multistatic spaceborne SAR") recently [6-8] proposed a mission scenario where a multi-static SAR is implemented using formation flying small satellite constellation. The proposed system is based on a master satellite transmitting a SAR signal which is in turn backscattered and received by a constellation of small satellites using DBF receive-only antenna systems. This approach allows reducing the overall mission costs while enabling lager freedom of operation and increasing the sensitivity due to the reduction of transmitter/receiver switches. Moreover, the proposed system is designed to define the most challenging scenario in terms of integration complexity of the radar sensor while targeting the highest resolution of the final EO system. Therefore, the SAR instrument proposed in DIFFERENT is fully polarimetric and it operates in two bands, namely X (9.6 GHz) [9] and Ka (35.75 GHz) [10-12], as shown in [13]. For the case at hand, the DBF module is used as a feed of an off-set reflector. The DBF array is thus composed of 60 digital channels connected to dual-band dualpolarized radiating elements. It is worth noticing that, to date, there is no available solution for SAR instruments targeting the proposed requirements. The multi-channel configuration allows to overcome the constraint of minimum antenna area, thus enabling the simultaneous

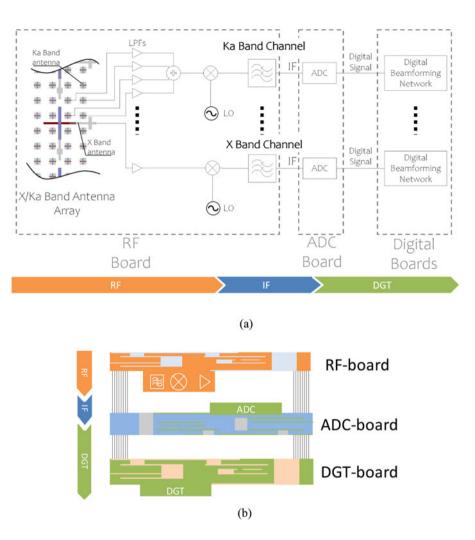


Fig. 1. SAR instrument architecture: (a) simplified representation of the building blocks; (b) stacked arrangement of the three boards.

acquisition of high-resolution and wide-swath (HRWS) radar images. Spatial sampling obtained with the multi-channel receiving technique provides additional samples between neighboring pulses in the slow-time domain, being this is equivalent to increase the pulse repetition frequency (PRF) of the system.

The single building blocks of the proposed SAR instrument were presented in [14] along with a definition of the intended mission scenario. In more recent work, the experimental validation of the integrated tile was proposed in [6, 9] reporting a complete characterization of the integrated analogue front-end. This work completes the dissemination of this activity by presenting the experimental results of the entire integrated system, including both analogue and digital blocks. Moreover, it will be shown how DBF processing can be used to perform a calibration [15, 16] of the multi-channel DBF SAR instrument compensating the inevitable imbalance present between the different channels which is also referred to as "channel error" or "channel mismatch."

Radar architecture

A schematic representation of the proposed SAR instrument is reported in Fig. 1. The main building blocks used in the prototype implementation (Fig. 1a) can be divided into three groups, depending on their functionality and frequency domain. For the Ka-band case, the RF signals coming from one polarization of a 2×2 subarray is amplified, combined and down-converted on-chip. The intermediate frequency (IF) signal is then filtered and digitalized using an Analog to Digital Converter (ADC). Finally, a DBF technique is used to combine the signals coming from different receiving channels. As shown in Fig. 1, a similar architecture is used for the X-band channels.

The radiating elements, the down-conversion chips and the filters are integrated into a specific board, namely the "RF-board". The 60 ADC channels are embedded into a dedicated panel referred to as "ADC-board" while the DGT processing is implemented using dedicated boards (i.e. DGT-boards). The boards are stacked on each other as shown in Fig. 1b.

- The 60 channels are organized in the following way:
- 24 channels for Ka-band horizontally polarized signals;
- 24 channels for Ka-band vertically polarized signals;
- 6 channels for X-band horizontally polarized signals;
- 6 channels for X-band vertically polarized signals

In this paper, the 60 channels of the manufactured SAR will be tested in order to demonstrate the ability of the radar to correctly acquire and elaborate the received data. The channels characterization presented in this paper is functional to the calibration process needed for the performance optimization of the radar. It is indeed well known that one of the key performance limitations for the DBF architecture at hand is related to the inevitable imbalance between the different channels. This imbalance can be

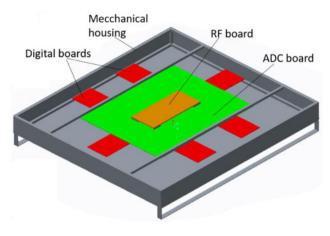


Fig. 2. Digital beamforming SAR.

caused by several reasons, such as the difference in antenna gains, unsynchronized sampling time between channels, and so on [8]. In this work, it will be shown how this problem can be solved by a proper calibration to be carried out before the DBF processing.

The radar includes 48 Ka-band channels plus 12 X-band channels. Figure 1 shows the architecture for one Ka-band channel and for one X-band channel. The experimental assessment of the analog part at component and sub-system level was already described in our previous papers [4, 5] and will be not repeated here. This paper is indeed focused on the digital and the full channel characterization.

The block diagram shown in Fig. 1a is implemented using one RF-board, one ADC-board and six digital boards as shown in Fig. 2. For test purposes, the boards are housed into a metallic box which embodies also the power supply and all the mechanical interfaces.

RF board

The RF board integrates into the same PCB the X/Ka-Band antenna array and the down-converting blocks as shown in Fig. 1, providing at the output 60 IF band channels.

The RF board is thus composed by:

- 96 Ka-band (24 in elevation times 4 in azimuth) dual-polarized (horizontal and vertical polarization) antenna elements;
- 6 X-band horizontally polarized antennas;
- 6 X-band vertically polarized antennas;
- 60 RF microwave monolithically integrated circuit (MMIC) units;
- Two local oscillators (LO) distribution networks (at X- and Ka-band).

Every 2×2 Ka-band subarray is actively combined to form a channel (see Fig. 1a). The function of the RF MMIC units is to down-convert the received V- and H-pol signals to an IF band. At X-band each antenna is connected to a single chip that down-converts the input signal. To reduce the complexity of the system, the X/Ka-band radiation elements, RF circuits and MIMCs are integrated into a single RF board. A detailed description and experimental validation of a single antenna tile of the RF board is provided in [6, 7] and will be not repeated here. Figure 3 shows the manufacture RF board. X- and Ka-band MMICs are located into cavities and wire bonded to the board.

ADC board

The down-converted signals at the output of the RF board are routed to the ADC board (Fig. 1), where 60 ADCs (one for each channel) are used to digitalize the baseband signals [17]. A 10 layers PCB stackup has been used to integrate all necessary

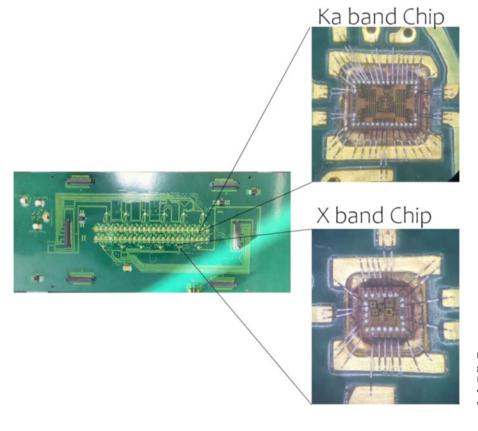
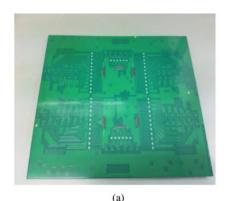


Fig. 3. Bottom view of the manufactured RF Board integrating 96 Ka-band antennas, 12 X band antennas, 48 Ka-band Chips and 12 X band Chips. The two MMIC providing down-conversion are also shown, including the wire bonding interconnections.



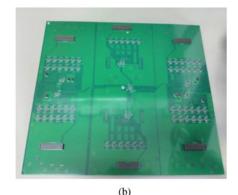


Fig. 4. ADC board: (a) top side (b) bottom side.

components, including band-pass filters and decoupling capacitors, on the same board.

High-speed digital signals are routed separately using the upper layers of the stackup to prevent coupling with the analog RF carriers on the lower part of the stackup. The IF signals are digitalized using the ADC ADS5527 by Texas Instruments which provide high-performance 12-bit, 210-MSPS A/D conversion.

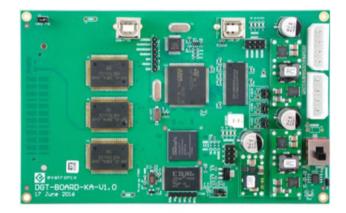
48 ADC is used for the Ka-band channels and 12 for the X-band channels. Figure 4 shows the manufactured ADC board. A multilayer [18] distribution network of equal length striplines have been used to guarantee signals synchronization.

An integrated differential clock distribution network has been realized in microstrip technology on the upper layer of the ADC board. This network was designed to guarantee clock coherence between the 60 ADCs. A clock generator (IDT FXO-LC535-210), placed on the center of the network, has been used to distribute a 210 MHz Low Voltage Differential Signal (LVDS) across the entire board. Low Skew Differential Clock Distribution Chips (IDT 8516) have been used for power division. The clock signal has been delivered to 60 ADCs and to the six digital boards via the ADC/DGT connectors. The final size of the manufactured ADC board is 923×425 mm.

Digital boards

In the DBF unit, the digitized data, corresponding to each antenna channel and polarization, are weighted and combined. The DBF is implemented in the six digital boards shown in Fig. 2. Four DGT boards are dedicated to the 48 Ka channels while two DGT boards are dedicated to the remaining 12 X-band channels. A 10-layer FR4 material stackup has been used to realize this board. The digital back-end consists of an ARM-based microprocessor, three ASICs, modules of clock synthesizer, FTDI module (FIFO to USB), SMA and FMC connectors. The DBF chips are manufactured using the low-cost 250-nm BiCMOS technology.

The DBF network is a cluster of individual DBF cores. The 60 channels require in total 16-DBF cores. The core can work in a static or in a dynamic mode. In the first case, the weights are fixed during operation; in the dynamic mode, the weights can be changed. The four different complex coefficients for each elevation beam are stored in a weight buffer. Next, the microprocessor adds up the sub-streams from each polarization (V and H) and band (X and Ka) and forms an output stream for a given azimuth beam. Figure 5 shows a manufactured Ka-band digital board.



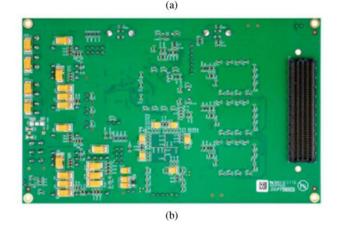


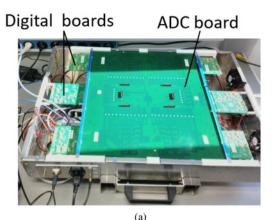
Fig. 5. Manufactured Ka-band digital board: (a) top view; (b) bottom view.

Assembled prototype

The whole SAR radar has been assembled within the shelter architecture of Fig. 2. Figure 6a shows the radar instrument with the upper cover removed where the ADC-board with the connected digital boards are visible. The RF board contains the antenna array and, for this reason, it has been placed outside the metallic box as shown in Fig. 6b.

Measurements

The manufactured prototype has been used to test the functionality of the 48 Ka-band channels and of the 12 X-band channels. As shown in Fig. 1, the block chain is composed of an analog part



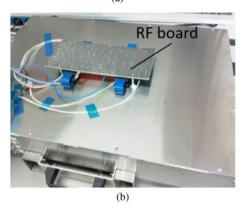


Fig. 6. Assembled radar demonstrator: (a) view with the upper part of the box removed, the ADC board with the connected digital boards are visible; (b) view with the upper part of the mechanical housing closed. The RF board is placed outside the metallic box.

followed by a digital part. Signals traveling in each channel are converted from analog to digital in the ADC-board. Two preliminary tests have been performed to characterize separately the analog and the digital parts of each channel. The analog part has been tested considering a single tile of the RF board, results are described in our previous papers [6, 7] and will be not repeated here. In this paper, the digital part has been tested removing the RF board and testing the sub-channel composed by the ADC board + digital board. In addition, a test of the full channel has been performed considering the entire chain shown in Fig. 1. Finally, a multi-channel test has been completed to demonstrate the ability of the beamforming network to combine signals coming from different channels in order to generate a single stream.

Digital sub-channel characterization

Characterization of the digital part of a channel has been performed removing the RF board from the radar. A known IF signal has been used to excite directly the ADC board using the semicircle shaped interface board shown in Fig. 7a.

The blocks involved in this test are shown in Fig. 7b. A 7 MHz sinewave has been generated and provided at the input of the ADC-board. A notebook connected to the output of the digital board has been used to elaborate the reconstructed signal. Figure 8 shows the spectrum of the reconstructed 7 MHz sinewave as seen at the output of the digital board. As expected, the reconstructed signal is concentrated around a single spectra element at 7 MHz. The maximum noise level is lower than -30 dB and, for this reason, it can be considered as negligible. The same test has been repeated 60 times to characterize all the channels of the radar.

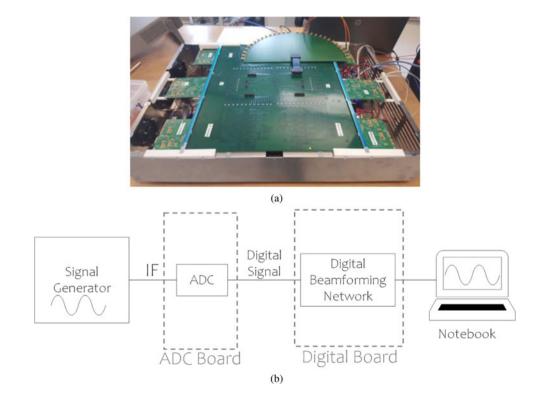


Fig. 7. Digital sub-channel measurement setup: (a) photo (b) block diagram.

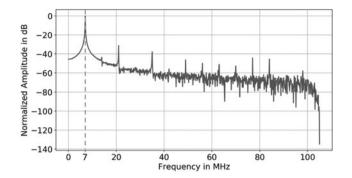


Fig. 8. Spectrum of the reconstructed 7 MHz sinewave at the output of the digital board.

Third-order nonlinear behaviors have been observed and they are mainly related to the implementation of the Hilbert Transform in the DGT boards. These effects do not have a relevant impact on the final system because they are mainly related to third-order harmonic generation. Nevertheless, several activities are in progress to get rid of them.

Full channel characterization

The purpose of this test is to characterize the full channel considering the whole block chain shown in Fig. 1. Figure 9 shows the measurement setup used for both X-band and Ka-band measurements. LO signals were generated with an external signal generator. Due to the high isolation between all antenna channels, tests could be performed independently for each polarization and for each band. For each polarization, the input signal was generated with a vector network analyzer and radiated with linearly polarized X- or Ka-band horn antennas. Isolation between the two polarizations is higher than 25 and 30 dB at Ka and X-band, respectively [7]. The coupling between the two bands is lower than 35 dB as two different radiator typologies are adopted in the different bands as in [11].

X-band channels characterization

As illustrated in Fig. 9, the characterization of the X-band channel was performed illuminating the radar with a horn antenna having

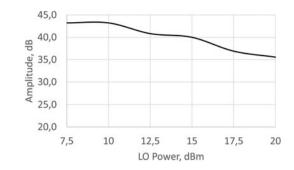


Fig. 10. Output amplitude measurement for one X-band channel versus LO power input.

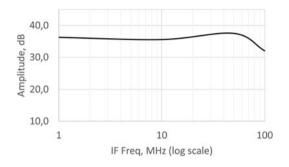


Fig. 11. Output amplitude measurement for one X-band channel versus frequency with 7.5 dBm LO power.

a gain of 22 dBi and placed 1.2 m above the RF board. A 9.6 GHz LO signal was given at the input. Output amplitude of X-band channels was firstly measured varying the power of the LO signal (see Fig. 10). The test shows that LO power providing the maximum output power is equal to 7.5 dBm. The optimum LO power was hence used to evaluate the frequency response reported in Fig. 11 where a maximum amplitude difference of 5 dB has been observed. Figure 12 shows the measured output for the six X-band channels associated with the horizontal polarization. Similar results have been obtained for the other polarization. For the experiment at hand, the maximum output difference between the difference can be ascribed to the chip to chip conversion

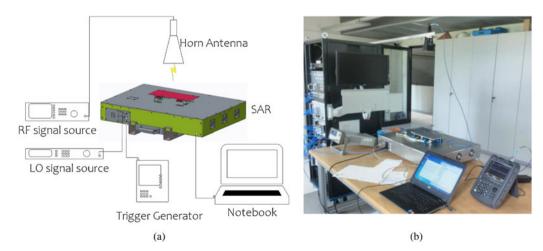


Fig. 9. Measurement setup: (a) block diagram (b) photo of the test environment.

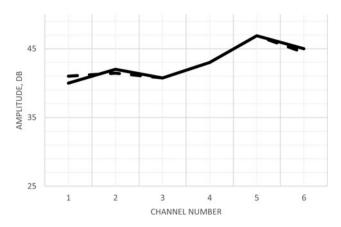


Fig. 12. Output amplitude measurement for the six X-band channels associated with the horizontal polarization (continuous line) and vertical polarization (dashed line).

 Table 1. Mean and standard deviation of the output amplitude measurement for the 12 X-band channels

	Horizontal polarization (dB)	Vertical polarization (dB)
Mean	42.9	42.9
Standard deviation	2.4	2.2

gain variability and part is because the horn illumination of the receiving surface is not uniform thus not providing the same input signal to all the antennas. Table 1 summarizes the mean and standard deviation of the output amplitude measurement for the X-band channels. It is worth noticing that channel to channel differences in the received and converted signal strength can be compensated during the system calibration, as it will be shown in the last part of this work.

Ka-band channels characterization

The characterization of the Ka-band channel was performed illuminating the radar with a horn antenna having a gain of 22 dBi placed 0.77 m above the RF board (Fig. 9). A 35.75 GHz LO signal with a power of 18 dBm was used in the input. The LO power providing the optimal conversion gain would be around 4 dB higher but this value could not be generated with the available instrumentation.

Figure 13 shows the output amplitude versus input frequency. As it can be observed, a maximum amplitude difference of 4.5 dBm is observed. As for the X-band assessment, this difference comes mainly from the ADC to board matching and in much smaller part from the antennas and receiver chip variability. Figure 14 shows the measured output of the 12 Ka channels

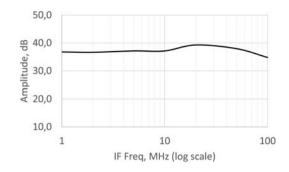


Fig. 13. Output amplitude measurement for one X-band channel versus frequency with 18 dBm LO power.

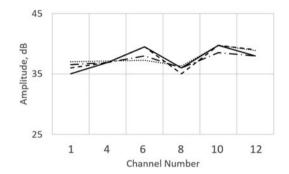


Fig. 14. Output amplitude measurement for 12 Ka-band channels associated with the: first horizontal polarization (point line), second horizontal polarization (dash-dot line), first vertical polarization (continuous line), second vertical polarization (dashed line).

associated with the horizontal polarization. The maximum observed inter-channel difference is of about 3.5 dB and its origin is similar to the X-band case. Mean and standard deviation of the output amplitude measurement for the Ka-band channels are summarized in Table 2.

Multi-channel assessment

A multi-channel experimental validation was performed to demonstrate the ability of the beamforming network to combine signals coming from different channels in order to generate a single stream. The test can be performed by arbitrarily choosing two channels and exciting them with a known input signal. For the case at hand, a sine wave has been used to excite the Ka-band array choosing channels 2 and 3 of the Ka-band horizontal polarization to perform the test. Signal coming from the two channels were combined in the BFN using different values of the complex weighting coefficients. Figure 15 shows the measured output amplitude of the sum of the selected channels versus phase shift. Phase shift was implemented using the complex weighting coefficients in the BFN. It can be observed that signals cancel almost

Table 2. Mean and standard deviation of the output amplitude measurement for the 48 Ka-band channels

	Horizontal polarization #1 (dB)	Horizontal polarization #2 (dB)	Vertical polarization #1 (dB)	Vertical polarization #2 (dB)
Mean	37.7	37.3	37.5	37.7
Standard deviation	1.2	0.9	1.73	1.81

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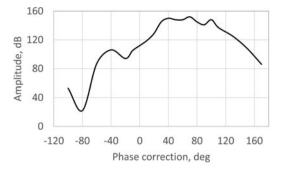


Fig. 15. Measured output amplitude of the sum of channel #2 and channel #4 of the Ka horizontal polarization versus phase shift.

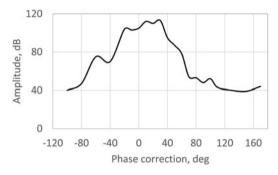


Fig. 16. Measured output amplitude of the sum of channel #1 and channel #5 of the X horizontal polarization versus phase shift.

each other for -80° (giving minimum sum) and are in phase around $+80^{\circ}$ (giving maximum sum). A similar result has been obtained if two random channels were chosen for the X band case. An example is shown in Fig. 16 where channels 1 and 5 of the X-polarization chain have been considered. In this case, the maximum sum is observed at about $+35^{\circ}$. This test is functional to a channels calibration process where all channels are considered and calibrated together. Hence, by iteratively performing this procedure between couples of antennas in the prototyped instrument it is possible to fully equalize the different channels.

Conclusion

This work presents the experimental validation of a highly integrated SAR instrument designed for next-generation EO satellite missions. The proposed configuration, whose design and intermediate validation was previously reported by the authors, is the most advanced SAR architecture proposed to date. It features a fully polarimetric reflector feed system operating in X- and Ka-band integrated into an antenna architecture entirely based on digital beam-forming. For the vertical and horizontal polarization, each antenna channel is down-converted, sampled via ADCs, processed using dedicated ASICs and transmitted via USB to a computer. In this work, the overall integrated prototype of the SAR instrument has been presented including the RF front end, the digital conversion blocks, and the digital processing boards. The validation of the integrated demonstrator has been proved through an extensive experimental assessment which included the X- and Ka-band channel characterization. Moreover, it has been shown how the amplitude differences which are unavoidably present in such type of assembly can be cancelled through a simple multi-channel calibration procedure.

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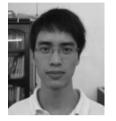
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