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Ge Epitaxy at Ultralow Growth Temperatures Enabled by a Pristine **Growth Environment**

Christoph Wilflingseder,* Johannes Aberl, Enrique Prado Navarrete, Günter Hesser, Heiko Groiss, Maciej O. Liedke, Maik Butterling, Andreas Wagner, Eric Hirschmann, Cedric Corley-Wiciak, Marvin H. Zoellner, Giovanni Capellini, Thomas Fromherz, and Moritz Brehm



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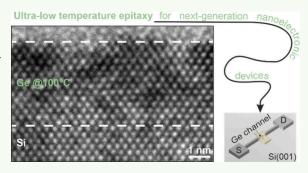
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ABSTRACT: Germanium (Ge), the next-in-line group-IV material, bears great potential to add functionality and performance to nextgeneration nanoelectronics and solid-state quantum transport based on silicon (Si) technology. Here, we investigate the direct epitaxial growth of two-dimensional high-quality crystalline Ge layers on Si deposited at ultralow growth temperatures ($T_{Ge} = 100-350$ °C) and pristine growth pressures ($\lesssim 10^{-10}$ mbar). First, we show that a decreasing $T_{\rm Ge}$ does not degrade the crystal quality of homoepitaxial Ge/Ge(001) by comparing the point defect density using positron annihilation lifetime spectroscopy. Subsequently, we present a systematic investigation of the Ge/Si(001) heteroepitaxy, varying the Ge coverage (Θ_{Ge} , 1, 2, 4, 8, 12, and 16 nm)



and $T_{\rm Ge}$ (100–300 °C, in increments of 50 °C) to assess the influence of these parameters on the layer's structural quality. Atomic force microscopy revealed a rippled surface topography with superimposed grainy features and the absence of three-dimensional structures, such as quantum dots. Transmission electron microscopy unveiled pseudomorphic grains of highly crystalline growth separated by defective domains. Thanks to nanobeam scanning X-ray diffraction measurements, we were able to evidence the lattice strain fluctuations due to the ripple-like structure of the layers. We conclude that the heteroepitaxial strain contributes to the formation of the ripples, which originate from the kinetic limitations of the ultralow temperatures.

KEYWORDS: germanium, silicon, epitaxy, strain, transmission electron microscopy, nanobeam X-ray diffraction, positron annihilation lifetime spectroscopy

INTRODUCTION

Silicon is the most widely used material for electronic devices, but for further technological progress, devices must become smaller, faster, more versatile, or less power-consuming. Since scaling down Si-based devices is becoming increasingly challenging, carefully chosen additions of materials have been explored. Ge stands out because of its high charge carrier mobility and lower band gap. Seminal work has been performed to integrate Ge on Si¹⁻⁹ for decades. Most implementations focused on Ge on Si and silicon-on-insulator (SOI)^{10,11} based on thick, strain-relaxed SiGe virtual substrates. 12-15 Using such strain engineering techniques and Ge as a channel material, it was shown to be possible to enhance the hole mobility to a record value of $\sim 4 \times 10^6 \frac{\text{cm}^2}{1000} \cdot 10^{-100}$

Ge, in general, broadens the spectrum of Si-based applications. With a band gap within the telecommunications C-band, Ge is the base for new perspectives in Si photonics, such as lasers, modulators, waveguides, and spintronics. ^{17–24} On another hand, compressively strained Ge layers emerged as a promising platform for solid-state quantum computing, and their cointegration on Si substrates offers a pathway toward a scaled,

functional quantum processor. 25-28 While all these devices rely on thick, relaxed, and thus defective Ge and SiGe buffer layers, recently an alternative approach was demonstrated. Strained and two-dimensional SiGe and Ge nanosheets were directly grown on Si and SOI substrates at low temperatures in the range of 270-350 °C using epitaxy. 29-32 Notably, the lowtemperature method permitted the growth of planar (Si-)Ge layers with an overcritical layer thickness that can be induced by kinetically limiting layer relaxation. Such thicker (Si-)Ge layers are essential for the top-down fabrication of versatile, high-quality nanoelectronics devices such as reconfigurable transistors³¹⁻³⁴ and devices based on negative differential resistance. 35,36 Besides these devices' excellent electronic characteristics, 31,32,34-36 detailed investigations of the critical

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growth parameters such as temperature, layer thickness, and limits of the strained growth have not been addressed. Due to the ~4.2% larger lattice constant of Ge compared to Si, only a ~1 nm thick Ge-wetting layer (WL) or less can be directly grown on a Si substrate for typical epitaxy temperatures ($300~^{\circ}\text{C} < T_{\text{Ge}} < 800~^{\circ}\text{C}$), with recent research being conducted at temperatures down to 200 °C.40 Beyond that thickness, the Stranski-Krastanow growth dynamics impose the formation of elastically relaxed 3D islands (usually referred to as quantum dot, QD) that eventually undergo a plastic relaxation process. 41,42 Lowering $T_{\rm Ge}$ limits QD formation kinetically and enables 2D growth of Ge, as demonstrated by Bean et al. 43 about 40 years ago by separating the growth of a Si buffer and Ge epilayer into two steps with different growth temperatures and reducing the $T_{\rm Ge}$ of the epilayer to 400 °C. In 1991, Eaglesham and Cerullo⁴⁴ grew planar Ge layers on Si at a T_{Ge} as low as 50 °C, discovering that films thicker than 3.5 nm were partially plastically relaxed, thanks to the formation of misfit dislocations (MDs). Within the $T_{\rm Ge}$ range of 50–150 °C, there is a maximum epilayer thickness that can be deposited, beyond which the growth resulted in the formation of an amorphous layer. Indeed, at lower temperatures the adatom surface diffusion on Si and on the Ge WL is reduced and residual atoms and molecules originating from the chamber background and the sources can be incorporated during growth, 45 leading to poor epitaxy.

In this work, we show the, often neglected, role of the deepultra-high vacuum conditions ($\leq 2.0 \times 10^{-10}$ mbar) in a molecular-beam epitaxy (MBE) chamber as a key enabler for expanding the growth parameter space of strained, epitaxial Ge thin layers deposited on either Ge or Si substrates. We first demonstrate that the crystalline quality of Ge/Ge(001) epilayers is almost unaffected by the deposition temperature $T_{\rm Ge}$ in the range 100–350 °C, as evidenced by variable energy positron annihilation lifetime spectroscopy measurements (VEPALS). Subsequently, we further rigorously studied the strained layer growth of Ge on Si, as a function of T_{Ge} and Ge layer thickness, Θ_{Ge} . Depending on T_{Ge} , the formation of large QDs in strained heteroepitaxy is suppressed, and the layers build domains of highly crystalline regions separated by thin areas of distorted growth. The findings are confirmed through various analytical techniques, including atomic force microscopy (AFM), transmission electron microscopy (TEM), X-ray diffraction (XRD), and scanning X-ray diffraction microscopy (SXDM). Consequently, ultra-low-temperature (ULT \equiv <350 °C) Ge epitaxy on Si(001) should be considered for further research with the aim of implementing it in industry MBE systems for next-generation nanoelectronics.

EXPERIMENTAL METHODS

Ultra-Low-Temperature Epitaxy. *Ge on Ge(001).* All samples were grown in a Riber SIVA-45 solid-source MBE system. For Ge homoepitaxy, Czochralski Ge(001) substrates were cleaned using a plasma cleaner and a UV ozone cleaner with intermediate immersion in solvents within an ultrasonic bath. Subsequently, the substrates were degassed at 300 °C for 30 min, and the oxide was removed through thermal desorption (750 °C for 10 min). Hereafter, for all samples, a 50 nm thick Ge buffer was grown at a $T_{\rm Ge}=320$ °C and at a rate of 0.02 nm/s. Within growth interrupts of 7, 16, 40, and 53 min, the sample temperature was ramped to 350 °C, 200 °C, 150 °C, and 100 °C. Once the required $T_{\rm Ge}$ was reached, 50 nm of Ge was deposited at a growth rate of 0.02 nm/s, maintaining the corresponding $T_{\rm Ge}$.

Ge on Si(001). The heteroepitaxial and strained Θ_{Ge} and T_{Ge} series were grown on high-resistivity (>10 k Ω cm), 4-in., and intrinsic floatzone Si(001) substrates. After wafer cleaning, including an RCA (Radio Corporation of America) cleaning process, the substrates were submerged for 1 min in diluted hydrofluoric acid (HF 1%) to remove the native oxide. Subsequently, the substrates underwent a two-step degassing process: 15 min at 700 °C and 30 min at 450 °C, before we grew a 75.5 nm thick Si buffer at growth temperatures that were linearly decreased from 650 to 600 °C, while the deposition rate was increased from 0.05 to 0.075 nm/s. Next, the substrates were cooled to T_{Ge} , i.e., 100 °C, 150 °C, 200 °C, 250 °C, and 300 °C, respectively. Depending on T_{Ge} , the growth interruption varied from 65 min (for 100 °C) to 13 min (300 °C). To increase the size of our sample set under controlled conditions, we created on each substrate three areas receiving different Θ_{Ge} values by switching off the substrate rotation and using a manual shutter to partially cover the substrate. In this way, a comprehensive matrix of 10 wafers was grown, featuring 6 varying Ge thicknesses Θ_{Ge} of 1, 2, and 4 nm, as well as 8, 12, and 16 nm for each of the five $T_{\rm Ge}$. The Ge deposition rate was 0.005 nm/s, and all samples remained uncapped. Two selected growth log files can be found in the Supporting Information (see Figures S1(a) and (b)), showcasing the highest and lowest growth pressure ($p_{\rm Ge}$) on average during Ge deposition (\sim 2 × 10⁻¹⁰ mbar and \sim 9 × 10⁻¹¹ mbar, respectively) for all samples.

■ LAYER CHARACTERIZATION

Variable Energy Positron Annihilation Lifetime Spectroscopy. VEPALS measurements were conducted at the monoenergetic positron source (MePS) beamline at HZDR, Germany. A CeBr₃ scintillator detector coupled to a Hamamatsu R13089-100 photomultiplier tube (PMT) was utilized for gamma photon detection. The signals were processed by the SPDevices ADQ14DC-2X digitizer (14-bit vertical resolution and 2GS/s horizontal resolution). The overall time resolution of the measurement system is approximately 0.25 ns, and all spectra contained at least 1 × 10^7 counts. A typical lifetime spectrum N(t), the absolute value of the time derivative of the positron decay spectrum, is described by

$$N(t) = R(t) \sum_{i=1}^{k+1} \frac{I_i}{\tau_i} e^{-t/\tau_i} + \text{Background}$$
(1)

where k different defect types contributing to the positron trapping are related to k + 1 components in the spectra with the individual positron lifetimes τ_i and intensities I_i ($\sum I_i =$ 1).⁴⁶ The instrument resolution function R(t) is a sum of two Gaussian functions with distinct intensities and relative shifts depending on the positron implantation energy, $E_{\rm p}$. It was determined by the measurement and analysis of a reference sample, i.e., amorphous yttria-stabilized zirconia (YSZ), which exhibited a single well-known lifetime component of ~0.182 ns. The background was negligible; hence, it was fixed to zero. All the spectra were deconvoluted using a nonlinear leastsquares fitting method, minimized by the Levenberg-Marquardt algorithm, employed within the fitting software package PALSfit⁴⁸ into two major lifetime components, which directly evidence localized annihilation at two different defect types (sizes; τ_1 and τ_2). Their relative intensities scale typically with the concentration of each defect type. In general, the positron lifetime increases with defect size and open volume size. The positron lifetime and its intensity have been probed as a function of the positron implantation energy E_p , which was recalculated to the mean implantation depth $\langle z \rangle$. The average positron lifetime τ_{av} is defined as $\tau_{av} = \sum_i \tau_i I_i$.

Atomic Force Microscopy. The surface topography of the samples was studied using AFM, using a Veeco Dimension 3100 AFM, equipped with OMCL-AC160TS-R3 cantilevers (Olympus Corporation), which had a probe radius of 7 nm. Operating in tapping mode, $1 \times 1~\mu\text{m}^2$ and $5 \times 5~\mu\text{m}^2$ micrographs with resolutions of 512 pixels/line were captured to determine both large-scale and small-scale features of the samples.

Transmission Electron Microscopy. The TEM experiments were carried out in a JEOL JEM-2200FS (JEOL, Japan) operated at an acceleration voltage of 200 kV. The TEM is equipped with an in-column Ω-filter and a TemCam-XF416 (TVIPS, Germany) CMOS-based camera. The conventional TEM investigations include high-resolution (HR)TEM as well as bright field (BF) and dark field (DF) imaging at two-beam conditions (TBC) of various diffraction reflexes. Plan-view (for examinations near the [001] zone axis) and cross-sectional (for examinations near a (110) zone axis) specimens were prepared classically by mechanical polishing (dimpling or wedgepolishing), followed by a final Ar-sputtering step to achieve electron transparency. Additionally, a ZEISS Crossbeam 1540XB (ZEISS, Germany) scanning electron microscope (SEM) with a focused ion beam (FIB) add-on was used to prepare the ready-to-use cross-sectional TEM lamellae.

X-ray Diffraction. Laboratory XRD measurements were performed with a Rigaku Smartlab diffractometer with a rotating-anode Cu K α source, a Ge(400) × 2 channel-cut beam monochromator, and an X-ray area detector. For each sample, we recorded a reciprocal space map (RSM) around the 004 and 224 Bragg reflections of the Ge quantum well (QW) layer to calculate the in-plane lattice strain $\varepsilon_{xx} = \varepsilon_{yy}$ and the out-of-plane strain ε_{zz} . These are the diagonal components of the strain tensor, which are linked through eq 2 by the Poisson number ν_{13} :

$$\varepsilon_{zz} = -\frac{(\varepsilon_{xx} + \varepsilon_{yy})}{\frac{1}{\nu_{13}} - 1} \tag{2}$$

We note that this equation is valid only in the assumption of no surface normal stress ($\sigma_{zz} = 0$); however, this approximation holds well for thin epitaxial layers near a free surface. We also determined the degree of relaxation according to eq 3:

$$R_{\rm D} = \frac{a_{\rm \parallel} - a_{\rm Si}}{a_{\rm Ge} - a_{\rm Si}} \tag{3}$$

Here, a_{\parallel} refers to the in-plane lattice constant of the Ge layer determined from XRD, while a_{Si} and a_{Ge} are the literature values for the lattice constants of Si and Ge, respectively. Laboratory XRD yields the strain state of the Ge QW layer averaged over a wide area on the sample due to the large width of the parallel X-ray beam. To probe the local strain landscape with fine spatial resolution, we employed an advanced synchrotron-based technique, scanning X-ray diffraction microscopy,⁵⁰ at the hard X-ray nanoprobe beamline ID01/ ESRF.⁵¹ The energy was set at 9 keV, and the beam was focused by a Fresnel zone plate (FZP) to a focal point of 25 nm. Diffraction maps of the 004 Bragg reflection from the Ge QW layer were recorded by scanning the sample across the beam in a (x, y) raster, while the intensity of the scattered Xrays was recorded continuously on a Maxipix area detector. To sample the 3D reciprocal space, these maps were measured for a series of rocking angles ω , yielding a five-dimensional (5D) data set. The diffraction data were analyzed with the SXDM

and Xrayutilities packages for Python, providing finely resolved maps of the local scattering vector Q_{004} for the 004 Bragg reflection, the c lattice parameter, and the vertical strain ε_{zz} according to eqs 4–6:

$$Q_{004} = \begin{bmatrix} Q_z \\ Q_y \\ Q_x \end{bmatrix} \tag{4}$$

$$c = \frac{1}{|Q_{004}|} \tag{5}$$

$$\varepsilon_{zz} = \frac{c}{a_0} - 1 \tag{6}$$

Moreover, the lattice rotation w_{yz} is calculated as the tilt of the scattering vector in reciprocal space according to eq 7:

$$w_{yz} = \tan^{-1} \left(\frac{Q_y}{Q_z} \right) \tag{7}$$

RESULTS AND DISCUSSION

Ge on Ge(001). At first, we investigated the influence of $T_{\rm Ge}$ on homoepitaxial Ge/Ge(001), thereby excluding any effects attributable to heteroepitaxial strain. Therefore, the defect microstructure in the Ge layers was evaluated using positron annihilation lifetime spectroscopy (PALS). The decomposition of the experimental positron lifetime spectra revealed two major defect contributions: the positron lifetime components τ_1 and τ_2 . The shortest lifetime τ_1 represents positron trapping and annihilation with electrons inside of small vacancy-like defects (single vacancies), whereas τ_2 originates from defect states in the subsurface region of the films (vacancy agglomerations). In the absence of a sufficient number of traps, positrons can diffuse freely, reaching eventually the surface or bulk.⁵² In Figure 1(a) we show the lifetimes τ_1 and τ_2 as a function of the $T_{\rm Ge}$ for a series of nominally identically thick Ge layers. In addition, the average

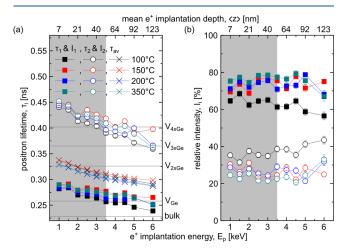


Figure 1. (a) Positron annihilation lifetime spectroscopy depth profiles of positron lifetimes τ_1 , τ_2 , and $\tau_{av.}$ and (b) their relative intensities I_1 and I_2 for Ge layers' MBE deposited at 100 °C, 150 °C, 200 °C, and 350 °C. The horizontal dotted lines denote calculated defect states for a Ge crystal. ⁵⁴ The gray areas denote the region of the top Ge layer.

lifetime au_{av} is plotted, which is sensitive to the overall defect size. For τ_{av} , the positron lifetime components are combined and weighted according to their relative intensities. These intensities are directly correlated to the concentration of defects and are presented in Figure 1(b). The PALS analysis shows a monotonic decrease of positron lifetime with the implantation depth, with a minimal influence of the deposition temperature. The decrease of positron lifetime across the depth is a consequence of a moderate number of positron traps (point defects, τ_1), which could hinder positron back diffusion to the surface (τ_2) , as well as a positron implantation profile, which broadens with its kinetic energy, i.e., depth.⁵³ The increase of τ_1 with deposition temperature indicates an onset of agglomeration of single point defects, preexisting in the 100 °C sample, and likely originates from the increased point defect density (I_1 raises with increased T_{Ge} in Figure 1(b)); hence a smaller fraction of positrons (τ_1) can reach and annihilate with surface states (τ_2) . We can safely assume that a shorter lifetime τ_1 represents a mixture of Ge monovacancy $(V_{Ge})^{54}$ with a small fraction of bivacancies (2 \times V_{Ge}), and a longer τ_2 arises from larger vacancy agglomerations (about four vacancies within a complex $4 \times V_{Ge}$) in the subsurface region (see Figure 1(a)). In general, there is virtually no difference in the point defect concentration depending on the growth temperature since the relative intensity does not change for larger temperatures. We notice that both τ_1 and τ_2 although irregularly, tend to mildly increase with T_{Ge} , pointing to an increasing fraction of bivacancies and to larger vacancy agglomerations, respectively.⁵⁴ On the other hand, the initial concentration of $V_{\rm Ge}$ ($I_1 \approx 65\%$) rises to $I_1 \approx 78\%$ with $T_{\rm Ge}$ (see Figure 1(b)), which indicates larger trapping at small point defects and an increase of their density. At the same time, I₂ concomitantly decreases as a smaller fraction of positrons can arrive at the subsurface region.

In summary, the PALS results demonstrate that for the unstrained growth of Ge on Ge in deep UHV, the growth temperature can be lowered from 350 °C to 100 °C without an increase in the density of point defects. We note that the significantly longer growth interruption for lower $T_{\rm Ge}$ also had no detrimental side effects on the layer quality. This finding contradicts the conventional wisdom that even homoepitaxial growth at very low temperatures eventually breaks down to form a polycrystalline or an amorphous phase. ⁴⁴ In turn, these results strongly indicate that a loss of crystallinity is instead a consequence of impurity incorporation due to poor $p_{\rm Ge}$ and a lack of contaminant desorption.

Ge on Si(001). In this section, we present the experimental results of the Θ_{Ge} and T_{Ge} series of Ge on Si(001), where the Ge rate was maintained at 0.005 nm/s. Following the deposition, the samples were inspected by using AFM to investigate the morphological surface variations. Figure 2(a) provides close-ups $(0.2 \times 0.5 \,\mu\text{m}^2)$ of the entire sample matrix, illustrating that even a Θ_{Ge} of 1 nm exerts a profound impact on the surface topography, in comparison to the vicinal surface of the high-T Si buffer replicated from the Si wafer (see Figure S2(a)). Indeed, we observe a pronounced surface ripple feature, highlighted in green in the image for a $\Theta_{Ge} = 1$ nm at $T_{\rm Ge}$ = 100 °C, across nearly all samples. The origin of this surface feature is the step-bunching of vicinal surfaces, although at the low $T_{\rm Ge}$ employed here, they have a less regular appearance than previously demonstrated.^{55–58} We analyzed the ripples by applying 2D FFTs to the 5 \times 5 μ m² micrographs (see Figure S3) and determined the peak

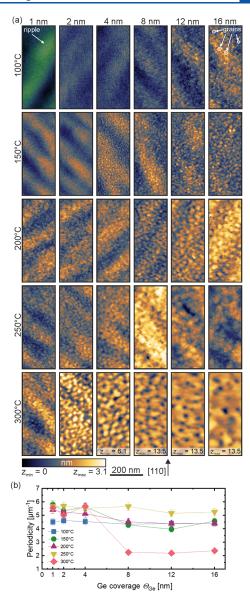


Figure 2. (a) $0.2 \times 0.5 \ \mu \text{m}^2$ close-ups of AFM images of all the strained samples, which have the same color coding except the 4, 8, 12, and 16 nm samples grown at 300 °C. (b) Periodicity of the ripples determined by 2D FFT of the $5 \times 5 \ \mu \text{m}^2$ micrographs. The error bars are a result of the analysis.

positions by Gaussian fitting. The periodicity of the ripples as a function of Θ_{Ge} and T_{Ge} (see Figure 2(b)) demonstrates no discernible trend for the majority of the samples, exhibiting a value of around 5 μ m⁻¹. However, for the samples deposited at $T_{\rm Ge}$ = 300 °C with $\Theta_{\rm Ge}$ = 8, 12, and 16 nm, we observe the disappearance of the ripples, as the periodicity falls to values below 2.5 μ m⁻¹. Instead, higher aspect ratio "mound-like" features emerge, which we attribute to enhanced adatom energy enabling their redistribution on the surface, reminding us of the Stranski-Krastanow dynamics. This is also confirmed by the root-mean-square (RMS) roughness values, which exceeded those of all the other samples and ranged from 0.8 to 1.5 nm. In addition, superimposed to the ripples, smaller grains are observed (see arrows in the figure of the sample with Θ_{Ge} = 16 nm at a $T_{\rm Ge}$ = 100 °C). For higher $T_{\rm Ge}$ and $\Theta_{\rm Ge}$, these grains increase in their lateral dimension. We note that for ultralow growth temperatures (e.g., 100 °C) a rather smooth

surface with an RMS roughness ranging from 0.24 to 0.36 nm was observed, i.e., close to that of pristine Si(001) wafers.

Based on the obtained changes in the surface morphology indicating an influence of $T_{\rm Ge}$ selected samples have been investigated via TEM. Figures 3(a) and (b) display DF images

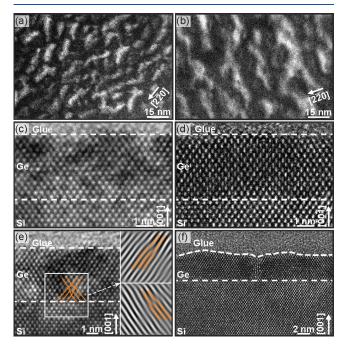


Figure 3. (a) Plan-view TEM from the 4 nm/100 °C sample recorded as DF at a 220 TBC sensitive to 220 plane variations. (b) Plan-view TEM of the 4 nm/250 °C sample recorded as DF at a 220 TBC sensitive to 220 plane variations. (c, e) Cross-section HRTEM from the 4 nm/100 °C sample. (d, f) Cross-section HRTEM 4 nm/250 °C sample.

recorded near a $2\overline{2}0$ TBC of the thin Ge layer. The perfect TBC is set at the position of the bright fringes where excitation errors are nonexistent. The contrast is therefore a measure of the $2\overline{2}0$ plane orientation variations. The planes are perfectly aligned with respect to the incident beam at the bright fringe positions, while between the fringes, the planes tilt away and back again to produce the given periodicity of the fringes. For the sample with a $\Theta_{\rm Ge}$ = 4 nm deposited at $T_{\rm Ge}$ = 100 °C, we estimate about 10.6 fringes per 100 nm, leading to an estimated 9.4 nm period; for the sample with an equal Θ_{Ge} but grown at $T_{\rm Ge}$ = 250 °C around 6.5 fringes per 100 nm are present, forming a larger periodicity of about 15.4 nm. Here we mention that usually DF images at the $2\overline{2}0$ TBC conditions are sensitive to misfit dislocation and their contrast should be visible. However, no clear dislocation contrast could be observed, which is attributed to the reason that the misfit dislocations are probably only present in short segments and end in internal (point) defects. There may be also other misfit defects that are not visible under the imaging conditions selected for the plan view specimens. Perfect Ge regions without misfit dislocation can be found in our samples, as displayed in Figures 3(c) and (d) for a Θ_{Ge} = 4 nm grown at $T_{\rm Ge}$ = 100 and 250 °C, respectively. However, these areas are separated by crystal sections containing extended defects with 60° perfect dislocations and twin/ Σ 9 defects. These examples can be seen in Figures 3(e) and (f). This proves that misfit dislocations are clearly present and typically built up

by two perfect 60° dislocations, as reported already for low $T_{\rm Ge}$ epitaxy. 60 If the two 60° dislocations can combine by the given thermic budget to a sessile Lomer dislocation or remain, two separated 60° dislocations cannot be unambiguously determined from these uncorrected phase contrast images. The other structure, the twin/ Σ 9 defect, arises during the coalescence of two Ge growth nuclei.⁵⁹ These misfit defects are responsible for the lattice plane variations observable in the plane-view DF images, thereby enabling an estimation of the defect densities from the observed fringe periodicities. The defect densities (dd) determined by this method are dd(4 nm/ $100 \,^{\circ}\text{C}$) = $1.1 \times 10^{12} \, \text{cm}^{-2}$ and $dd(4 \, \text{nm}/250 \,^{\circ}\text{C}) = 4.2 \times 10^{11}$ cm⁻². However, we note that the formation of threading dislocations in their typical configurations was not observed. Consequently, the estimated values must be understood as the total defect density.

The lattice strain of the epitaxial layers was systematically investigated by XRD measurements, allowing the tracking of the heteroepitaxial strain in dependence on Θ_{Ge} and T_{Ge} . Figure 4(a) presents an RSM around the 004 Bragg reflections of a sample with $\Theta_{\rm Ge}$ = 4 nm and $T_{\rm Ge}$ = 100 °C. We observe an intense peak from the Si substrate at an out-of-plane momentum transfer $Q_z = 7.365 \text{ nm}^{-1}$. Below, there is the signal from the Ge layer, which is elongated along Q, as the thin film comprises only ~32 atomic layers. Along the in-plane momentum transfer Q_w which for a symmetric reflection corresponds to the rocking angle ω , this Bragg peak is narrow since the epilayer consists of pseudomorphic grains. Moreover, we observe fringes stemming from intensity modulation along Qz due to interference of X-rays reflected from the top and bottom surfaces of the Ge layer. An RSM for the $\Theta_{\rm Ge}$ = 8 nm layer deposited at T_{Ge} = 100 °C is shown in Figure 4(b). Interestingly, for this thicker layer, we observe a "halo" of diffuse scattering along the sharp peak. This phenomenon is attributed to the onset of plastic relaxation, e.g., misfit dislocations limited to a certain amount of grains and the growth on top of twin/ Σ 9 defects. Both of these processes release the global biaxial strain in Ge by inducing local fields of strain and rotation. This corresponds to a broadening of the spatial distribution of the lattice spacing, which leads to diffuse X-ray scattering. While the position of the 004 Bragg reflection is sensitive only to the out-of-plane strain, asymmetric Bragg reflections allow us to understand the 3D deformation of the Ge unit cell. In Figures 4(c) and (d), we show 224 RSMs for the two samples. Interestingly, for the 4 nm Ge layer in panel (c), we find two Bragg peaks stemming from the Ge layer. One is a signal at the in-plane momentum transfer of the Si substrate at $Q_x = 5.205 \text{ nm}^{-1}$, as the metastable Ge epilayer adapts its in-plane lattice parameter. However, there is also a secondary, diffuse peak shifted toward smaller Q_r and larger Q_z . The simultaneous presence of the two peaks indicates that within this thin layer, some amount of Ge remains perfectly pseudomorphic, while other domains are distorted and partially relaxed, indicating structural defects. This observation is in agreement with what can be concluded from the TEM measurements carried out on the same sample (see Figures 3(a),(c),(e), which revealed the presence of misfit dislocations and domain formation due to the coalescence of two grains. For the 8 nm-thick Ge layer (see Figure 4(d)), the Bragg peak from the pseudomorphic material is less intense, while the signal for the relaxed layer becomes even more diffuse and moves toward the position for a cubic lattice, indicated by the red line. From the linear combination of the

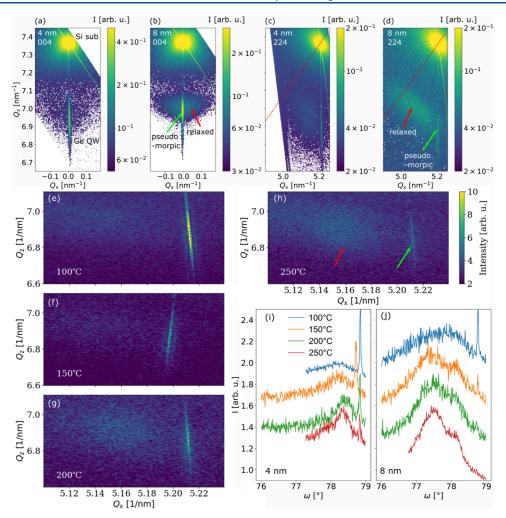


Figure 4. Characterization of ultrathin epitaxial Ge/Si layers by XRD. (a) 004 RSM of a 4 nm Ge layer grown at T = 100 °C. (b) 004 RSM of an 8 nm Ge layer at 100 °C; the red and green arrows indicate the intensity diffracted from relaxed and pseudomorphic regions, respectively. (c) 224 RSM for 4 nm Ge at 100 °C. The red line is the direction indicating a perfect cubic lattice. (d) 224 RSM for 8 nm Ge at 100 °C. (e-h) 224 RSMs around the Ge signal for 4 nm Ge layers grown at different temperatures. (i, j) 224 rocking curves across the Ge signals for 4 and 8 nm thick layers.

004 and 224 Bragg peaks, we calculate the lattice strains $\varepsilon_{xx} = \varepsilon_{yy}$ and ε_{zz} for the partially relaxed layer peaks by the equations provided in ref 61, assuming the epilayer as pure Ge. In this way, we calculate the strains of the domains for a series of samples listed in Table 1, excluding the pseudomorphic regions.

As expected, the strain decreases with increasing layer thickness and growth temperature as the metastable epilayer undergoes additional plastic relaxation when the thermal activation energy is available. We note that for the peaks of the

Table 1. Strain State of the Ge Domains Determined by XRD

Sample	ε_{xx} [%]	ε_{zz} [%]	$ u_{13}$	R_{D} [%]
8 nm 100 °C	-1.53	0.89	0.226	61.9
8 nm 150 °C	-1.39	0.81	0.224	65.3
8 nm 200 °C	-1.41	0.85	0.232	64.9
8 nm 250 °C	-1.26	0.75	0.229	68.5
12 nm 150 °C	-1.12	0.62	0.217	72.0
12 nm 200 °C	-1.15	0.68	0.230	71.4
16 nm 100 °C	-1.04	0.55	0.209	74.0
16 nm 150 °C	-0.91	0.55	0.231	77.4

pseudomorphic material, the in-plane strain is set by the lattice mismatch of Ge to the Si substrate, i.e., $\varepsilon_{xx} \cong 4.0\%$. Moreover, we determine the Poisson number by eq 2, finding that in all samples it is smaller than the literature value of \sim 0.273. This is consistent with previous observations that the Poisson number in highly strained epitaxial thin films is smaller than in bulk materials, 62 which may be attributed to either a change in the elastic parameters or the breakdown of linear elastic theory for non-infinitesimal strains. ⁶³ When considering the degree of relaxation for each sample in Table 1, it is apparent that the average degree of relaxation R_D of the domains is increasing with Θ_{Ge} . Except for the layers at T_{Ge} = 150 °C, the trend of R_{D} with increased T_{Ge} is comparable. In Figures 4(e)-(h), we show reciprocal space around the 224 Bragg diffraction from the Ge layers for the Θ_{Ge} = 4 nm sample series at different T_{Ge} = 100–250 $^{\circ}$ C. We observe that with larger $T_{\rm Ge}$, the intensity from the pseudomorphic peak decreases, while the diffuse signal stemming from the defective regions becomes more prominent. In Figures 4(i) and (j), rocking curves are plotted as a function of the rocking angle ω across the 224 Ge peaks for the 4 and 8 nm sample series. Also, here we observe that the ratio of the intensity of the pseudomorphic peak to that of the diffuse peak decreases with $T_{\rm Ge}$ and $\Theta_{\rm Ge}$, as more of the material undergoes relaxation. Moreover, for the 8 nm samples

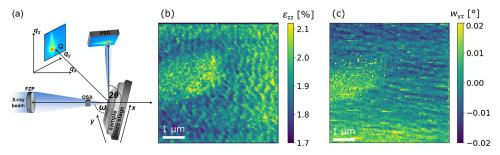


Figure 5. (a) Schematic setup for SXDM experiments at ID01/ESRF, reproduced with permission from ref 25. (b) SXDM map of the ε_{zz} strain component. (c) SXDM map of the w_{vz} lattice rotation.

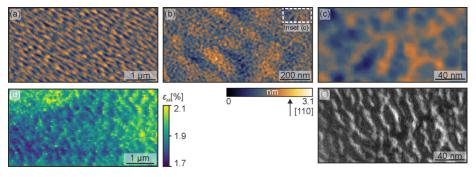


Figure 6. Comparison of the measurements for the sample with a $\Theta_{\rm Ge}$ = 4 nm deposited at $T_{\rm Ge}$ = 250 °C. (a) 5 × 2.5 μ m² AFM image showing the surface ripples. (b) 1 × 0.5 μ m² AFM micrograph depicting ripples and superimposed grains. (c) Inset of (b) putting the focus on the grains. (d) SXDM map of the ε_{zz} strain component. (e) Plan-view TEM DF image sensitive to 220 plane variations.

grown at $T_{\rm Ge}$ > 100 °C, the formation of a secondary peak in the defective regions is apparent, corresponding to different degrees of relaxation within the same layer.

In order to study the spatial strain fluctuations, we investigated the 4 nm Ge layer grown at 250 °C by SXDM, 50 at the hard X-ray nanoprobe beamline ID01/ESRF, 51 as sketched in Figure 5(a). Thus, we obtain a spatial map of ε_{zz} with \sim 50 nm resolution, presented in Figure 5(b). The dominant features in the map take the form of semiregular undulations reminiscent of the domains observed in the AFM images. Furthermore, the map of w_{yz} obtained from the SXDM data is presented in Figure 5(c), showing a regular pattern of step-like undulations.

The TEM and SXDM results indicate the presence of strain relaxations on different length scales. To gain a comprehensive understanding of the surface morphology, Figure 6 presents a comparison of the measurements for the sample with a $\Theta_{Ge} = 4$ nm at T_{Ge} = 250 °C. Figure 6(a) depicts an AFM height image $(5 \times 2.5 \ \mu \text{m}^2)$ and illustrates the larger surface structure. These ripples correspond well with the observed structure in the SXDM ε_{zz} strain map (see Figure 6(d)). As previously discussed, the sample's surface also exhibits granular features, which can be observed in Figure 6(b). It shows an AFM image of a smaller size (1 \times 0.5 μ m²). The inset of this AFM image and the plan-view TEM DF image in Figures 6(c) and (e), respectively, complete the picture of the surface. Although the size of the structures observed in the AFM image is slightly larger than the size of the TEM structures due to the lower resolution of the AFM, the results agree very well.

The reduction in the appearance of ripples at higher $T_{\rm Ge}$ (=300 °C) is consistent with previous studies that have demonstrated that step-bunching is kinetically driven and occurs in Si homoepitaxy without strain, too. ^{64,65} We have additionally demonstrated the formation of ripples in a 50 nm

thick Si epilayer grown at 200 $^{\circ}$ C on a high-T Si buffer (see Figure S2(b)). Notably, the morphology of the Si ripples differs from that observed in the Ge samples, suggesting a strain-related contribution to the formation process. This observation aligns with the SXDM results, which indicate that the ripples influence the strain distribution within the sample.

Our findings of a granular surface corroborate the results of Storozhevyk et al., demonstrating that under a flux of Ge atoms at ULT, no islands form; instead, a surface composed of pseudomorphic Ge clusters emerged. 66 The kinetic limitations at ULT prevent the formation of larger islands, leading to the coalescence of Ge clusters into grown-in twin/ Σ 9 defects, which relaxes strain (see Figure 3(f)).⁵⁹ A notable feature is that even with increased Ge deposition, the epilayers remained island-free and crystalline. Even at very low T_{Ge} , the grains formed were all pseudomorphic, resulting in a more intense XRD peak at the position of the Si substrate peak in the 224 RSM. Incoming Ge atoms diffused to the nearest relaxed position, typically the defect between two grains. At higher T_{Ge} , the adatoms have more energy, allowing for longer diffusion distances. Consequently, the grains broadened for both more Θ_{Ge} and higher T_{Ge} with Ge atoms positioning within a range of 0.543-0.566 nm, corresponding to the lattice constants of Si and Ge, respectively. This broad distribution results in the diffuse signal observed in XRD. We note that for ultralow temperature growth at excellent p_{Ge} , the absence of epitaxial growth breakdown via the formation of amorphous layers represents a crucial finding. It clearly shows that epitaxial Ge layers can be grown with a thickness relevant for nanoelectronics applications. Indeed, 4 nm-thick Ge layers deposited using similar growth conditions were recently implemented on thin silicon-on-insulator substrates for nanoelectronic device applications, such as reconfigurable transistors. 32,34,35 These devices clearly outperformed reference

devices based on pure SOI, Ge-on-insulator substrates, ³² and devices based on harvested Ge VLS nanowires. Interestingly, in these Si/Ge/Si on-insulator nanosheets, no defects such as the here-observed grown-in twin/ Σ 9 defects were found, even if the Ge layers had been grown on the Si device layer at similar $\Theta_{\rm Ge}$ and $T_{\rm Ge}$. We note that the whole Si/Ge/Si nanosheet structure on the insulator was very thin in refs 31, 32, and 34–36, i.e., <35 nm thick. Thus, effects like strain partitioning (compliance) between Si and Ge layers ⁶⁷ and the role of tensile strain induced by the Si/SiO₂ interface after thermal oxidation ⁶⁸ could influence the relaxation dynamics of the Ge layers. Consequently, the difference in the growth of Ge layers on bulk Si and thin-SOI must be further investigated.

CONCLUSION

We investigated the growth characteristics of Ge layers on Ge(001) and Ge layers on Si(001) using MBE at ultra-lowgrowth temperatures by varying the layer thickness well beyond the limits for elastic relaxation at conventional high growth temperatures. For the homoepitaxy, VEPALS investigations demonstrate that even at $T_{Ge} = 100$ °C, highly crystalline growth is possible if the background pressure during the growth is kept low. For the strained heteroepitaxy, AFM revealed surface ripples with superimposed grains for T_{Ge} < 300 °C or small $\Theta_{\rm Ge}$ at $T_{\rm Ge}$ = 300 °C. Plan-view DF TEM and HRTEM showed that these grains coalesce into twin/ Σ 9 defects, relieving misfit stress with misfit dislocations. Despite these defects, the Ge layers within grains exhibited a high crystalline quality, pseudomorphic characteristics, and expected strain. XRD measurements confirmed both distorted and crystalline growth through two distinct peaks. The strain state and the $R_{\rm D}$ of the relaxed regions exhibited an increase with Θ_{Ge} and T_{Ge} . The kinetic limitations of the ULT growth were identified as the cause of the ripples, with strain influencing their formation. This was demonstrated by nanobeam X-ray diffraction measurements, which indicated strain variations in regions with and without ripples. The incorporation of the aforementioned defects formed upon epitaxy of Ge on Si is rather a result of the high strain in the layers than of the ultralow growth temperature per se. Thus, strain management through, for example, the use of strained-SOI substrates³¹ or selective epitaxy on ridges^{69,70} or nanotips⁶⁷ combined with ULT growth in deep UHV can be the route for implementing high-quality, supersaturated Ge layers on Si for novel device applications. These findings of Ge growth at ultra-low-growth temperatures contribute to the feasibility of scalable top-down fabrication techniques for Ge-based nanoelectronic devices. 32,35 This development opens potential opportunities for further technological advancements in next-generation semiconductor technologies.

ASSOCIATED CONTENT

Supporting Information

The Supporting Information is available free of charge at https://pubs.acs.org/doi/10.1021/acsaelm.4c01678.

Two growth log files including the worst and the best vacuum conditions; AFM images of the high-T Si buffer and low-T Si homoepitaxial sample; AFM with dimensions of $5 \times 2.5 \ \mu \text{m}^2$ of the entire heteroepitaxial sample matrix; figure highlighting the importance of ultralow pressures for ULT epitaxy (PDF)

AUTHOR INFORMATION

Corresponding Author

Christoph Wilflingseder — Institute of Semiconductor and Solid State Physics, Johannes Kepler University Linz, 4040 Linz, Austria; oorcid.org/0009-0008-2946-1069; Email: christoph.wilflingseder@jku.at

Authors

Johannes Aberl – Institute of Semiconductor and Solid State Physics, Johannes Kepler University Linz, 4040 Linz, Austria Enrique Prado Navarrete – Institute of Semiconductor and Solid State Physics, Johannes Kepler University Linz, 4040 Linz, Austria

Günter Hesser – Christian Doppler Laboratory for Nanoscale Phase Transformations, Center for Surface And Nanoanalytics (ZONA), Johannes Kepler University Linz, 4040 Linz, Austria

Heiko Groiss – Christian Doppler Laboratory for Nanoscale Phase Transformations, Center for Surface And Nanoanalytics (ZONA), Johannes Kepler University Linz, 4040 Linz, Austria

Maciej O. Liedke – Helmholtz-Zentrum Dresden-Rossendorf e.V., Institute of Radiation Physics, Dresden 01328, Germany; orcid.org/0000-0001-7933-7295

Maik Butterling — Helmholtz-Zentrum Dresden-Rossendorf e.V., Institute of Radiation Physics, Dresden 01328, Germany Andreas Wagner — Helmholtz-Zentrum Dresden-Rossendorf e.V., Institute of Radiation Physics, Dresden 01328, Germany Eric Hirschmann — Helmholtz-Zentrum Dresden-Rossendorf e.V., Institute of Radiation Physics, Dresden 01328, Germany Cedric Corley-Wiciak — ESRF — European Synchrotron Radiation Facility, 38043 Grenoble, Cedex 9, France

Marvin H. Zoellner – IHP – Leibniz-Institut für innovative Mikroelektronik, D-15236 Frankfurt, (Oder), Germany; orcid.org/0000-0001-7204-1096

Giovanni Capellini – IHP – Leibniz-Institut für innovative Mikroelektronik, D-15236 Frankfurt, (Oder), Germany; Dipartimento di Scienze, Università Roma Tre, 00146 Roma, Italy

Thomas Fromherz — Institute of Semiconductor and Solid State Physics, Johannes Kepler University Linz, 4040 Linz, Austria; orcid.org/0000-0003-2718-4041

Moritz Brehm — Institute of Semiconductor and Solid State Physics, Johannes Kepler University Linz, 4040 Linz, Austria; ⊚ orcid.org/0000-0002-5629-5923

Complete contact information is available at: https://pubs.acs.org/10.1021/acsaelm.4c01678

Author Contributions

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Notes

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