

# Waveguide-Coupled Ge Photodiodes with 3-dB Bandwidth $\geq 110$ GHz

Stefan Lischke  
Technology / High-Performance Si-Technologies  
IHP – Leibniz-Institut für innovative Mikroelektronik)  
Frankfurt (Oder), Germany  
lischke@ihp-microelectronics.com

Anna Peczek  
IHP Solutions GmbH  
Frankfurt (Oder), Germany  
anna.peczek@ihp-solutions.com

Daniel Steckler  
Technology / Silicon Photonics  
IHP – Leibniz-Institut für innovative Mikroelektronik)  
Frankfurt (Oder), Germany  
steckler@ihp-microelectronics.com

Falk Korndörfer  
Technology / Electrical Characterization  
IHP – Leibniz-Institut für innovative Mikroelektronik)  
Frankfurt (Oder), Germany  
line 5: email address or ORCID

Christian Mai  
Technology / Silicon Photonics  
IHP – Leibniz-Institut für innovative Mikroelektronik)  
Frankfurt (Oder), Germany  
cmai@ihp-microelectronics.com

Lars Zimmermann  
Technology / Silicon Photonics  
IHP – Leibniz-Institut für innovative Mikroelektronik)  
Frankfurt (Oder), Germany  
lzimmermann@ihp-microelectronics.com

**Abstract**— We present a photodiode construction in which germanium is sandwiched in between complementary in-situ doped silicon layers. With this new approach we demonstrate photodiodes with optoelectrical 3-dB bandwidth of 110 GHz and responsivity of 0.6 A/W at 1550 nm, obtained at reverse bias of 2 V.

**Keywords**—Germanium, photodiode, photodetector, silicon, photonics, PIC, ePIC

## I. INTRODUCTION

Progress of silicon photonics technology has been an important enabler for datacenter interconnect or metro applications in recent years. A decisive factor has been the development of devices with optoelectrical (OE) bandwidth exceeding 50 GHz, allowing for generation and detection of signals approaching 100 GBaud. Nevertheless, further enhancement of OE bandwidth is highly desirable in view of anticipated symbol rates of 140-200 GBaud. In this paper we focus on silicon photonic detectors, i.e. waveguide (WG) coupled germanium photodiodes (PDs). Already in 2015, Ge PDs with  $>67$  GHz OE bandwidth and responsivity of 0.9 A/W could be demonstrated [1]. However, further Ge PD improvement has been impeded since and 67 GHz OE bandwidth remained the benchmark until now. In this article, we demonstrate for the first time realization of PIC/EPIC compatible Ge PDs with OE bandwidths exceeding 110 GHz, which show responsivity values  $>0.6$  A/W, approaching the performance of state-of-the-art III-V photodiodes [2, 3]. In [1, 4] the negative impact of minority carrier diffusion on the frequency response of Ge PDs has been discussed. Two methods have been identified to reduce this contribution and increase the PD bandwidths: (1) Manipulation of minority carrier lifetimes by incorporation of non-doping elements and (2) Reduction of the fraction of carriers that are subject to diffusion by shrinking the doped Ge regions, such that less photo carriers are generated in doped regions and more photo carriers are generated in the intrinsic region. As our prior PD relies on ion-implantation into Ge [1], the negative impact of minority carrier diffusion cannot be diminished easily.

Recently, we presented a novel Ge PD design in which we entirely omit ion-implantation [5]. Instead, complementary in-situ doped Si regions sandwich an intrinsic Ge region, such that a lateral p-i-n diode is realized. By this approach we aim on avoiding photo carrier generation in doped Ge (Fig. 1a). In contrast to vertical drift field PDs, the Si WG below the Ge remains un-doped in our approach, which shall be beneficial for the responsivity. Similar to the prior PD [2], the lateral Si offshoots allow for low-ohmic contacting utilizing state-of-the-art silicide processes and allow for placing the metal contacts at a certain distance to the optical mode.

## II. MEASUREMENT RESULTS AND DISCUSSION

Normalized frequency responses of PDs with nominal Ge widths of about 300 nm (referred to hereinafter as “Ge-300”) measured at chip level with a Keysight 110 GHz lightwave component analyzer (LCA) on four chips is shown in Fig. 1b. Clearly, the presented PDs achieve OE 3-dB bandwidths of 110 GHz and above. Fig. 1c depicts normalized frequency response of one PD measured at different bias conditions and proves a large bandwidth of 70 GHz already at -0.25 V. The large difference between zero-bias and -0.25 V bandwidths indicates that some undepleted regions remain where minority carrier diffusion contributes. However, at low reverse bias, those regions seem to be widely depleted. Internal responsivity at 1550 nm is estimated to about 0.64 A/W at 1550 nm and TE polarization.

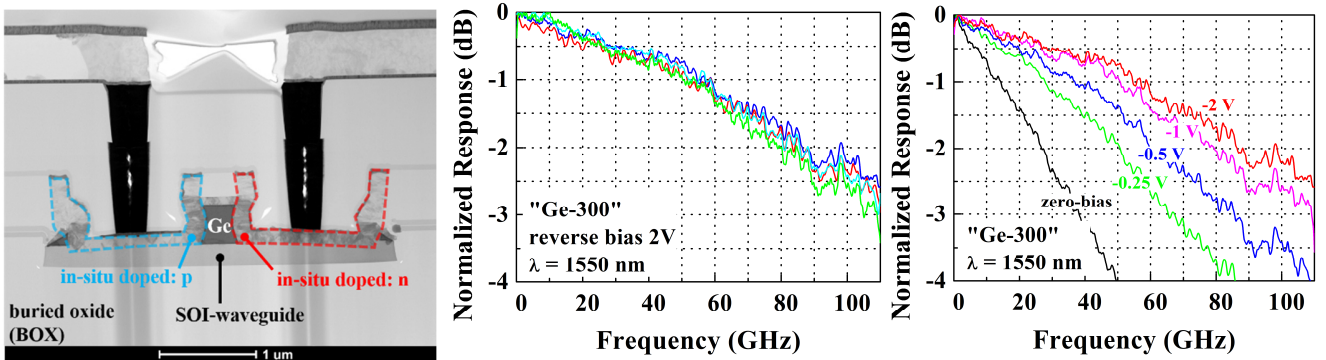


Fig. 1. (a) TEM image of the novel Ge PD with complementary in-situ doped silicon contact regions (indicated by different color). Image shows fabrication up to the first metal layer; cross-sections cut perpendicular to the light incidence direction, normalized frequency responses of Ge PDs from four chips at reverse bias of 2 V (b) as well as at different reverse bias conditions (c).

The optical power handling capability of PD “Ge-300” is demonstrated in Fig. 2a. By varying the laser input power the frequency response is measured at increased photo currents. Up to a photo current of about 1.2 mA, the PD achieves high bandwidth  $>100$  GHz. Further increase of the optical power causes a degradation of the bandwidths such that at 3 mA the OE bandwidth drops to about 60 GHz. Compared to our prior PD, this is an improvement of a factor 1.5 [6]. In Fig. 2b dark currents of “Ge-300” are compared to those of our prior PD generation [2], showing that in tendency the level and scattering is increased for our novel PDs. The inset, a box-plot, summarizes dark currents of “Ge-300” and a slightly broader diode named “Ge-350”. Measurements with the 110 GHz LCA were performed at chip level, investigations on a full wafer could therefore not be performed. Instead, a 67 GHz LCA was available for wafer level characterization, however, this LCA does not allow for the estimation of the 3-dB bandwidths. We therefore monitor the 1-dB bandwidths of a full wafer (Fig. 2c).

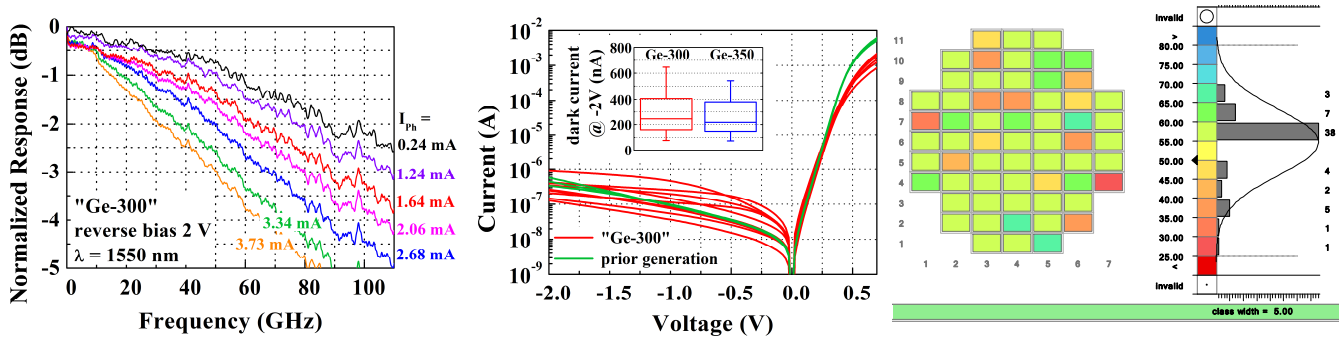


Fig. 2. (a) Normalized frequency responses of one “Ge-300” PD at varied optical input power to increase the photo currents; (b) Room temperature IV characteristics from 9 wafer sites compared to prior PDs (3 wfr. sites). Box-plots as inset show dark currents of “Ge-300” and “Ge-350” (each from on 61 wfr. sites). Lengths of all PDs are 20  $\mu$ m. (c) Wafermap and histogram of 1-dB OE bandwidths at -2 V (in GHz), measured on 62 wafer sites.

### III. CONCLUSION

We presented an SOI waveguide coupled Ge PD with very high OE 3-dB bandwidth of  $>110$  GHz at reverse bias of 2 V. This performance gain was achieved by a novel construction in that Ge is sandwiched in between two complementary in-situ doped Si regions. By avoiding ion-implantation into the Ge minority carrier diffusion effects are suppressed. A responsivity of  $>0.6$  A/W at 1550 nm is achieved, while the dark current of this device yields to about 300 nA (both at -2 V). Compared to our prior PD, the new diodes show improved optical power handling capability as well. To our knowledge, this is the most advanced germanium photo detector in terms of very high bandwidth combined with state-of-the-art responsivity as well as moderate dark currents. We demonstrated that the new PDs can be fabricated with high yield.

### REFERENCES

- [1] S. Lischke et al., Optics express, vol. 23, no. 21, pp. 27213–27220, 2015, doi: 10.1364/oe.23.027213.
- [2] 100 GHz Single High-speed Photodetector | II-VI Incorporated. [Online]. Available: <https://optical.communications.ii-vi.com/communication-components/xpdv412xr> (accessed: 25.08.20).
- [3] P. Runge, G. Zhou, F. Ganzer, S. Mutschall, and A. Seeger, in 2015 European Conference on Optical Communication (ECOC), 2015, pp.1-3.
- [4] S. Lischke et al., in 2014 IEEE Bipolar/BiCMOS Circuits and Technology Meeting (BCTM), Coronado, CA, USA, 28.09.14 - 01.10.14, pp. 29–32.
- [5] S. Lischke et al., “Ge Photodiode with -3 dB OE Bandwidth of 110 GHz for PIC and ePIC Platforms,” in 2020 IEEE International Electron Devices Meeting (IEDM), San Francisco, CA, USA, Dec. 2020 - Dec. 2020, 7.3.1-7.3.4.
- [6] D. Knoll et al., in 2015 IEEE International Electron Devices Meeting (IEDM), Washington, DC, 07.12.15 - 09.12.15, 15.6.1-15.6.4.